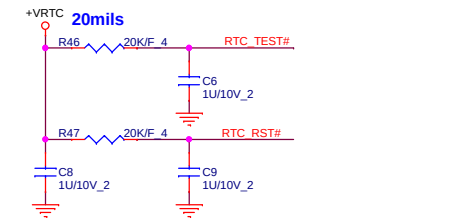
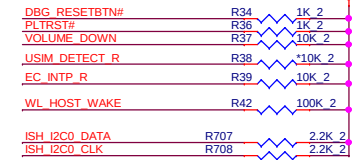
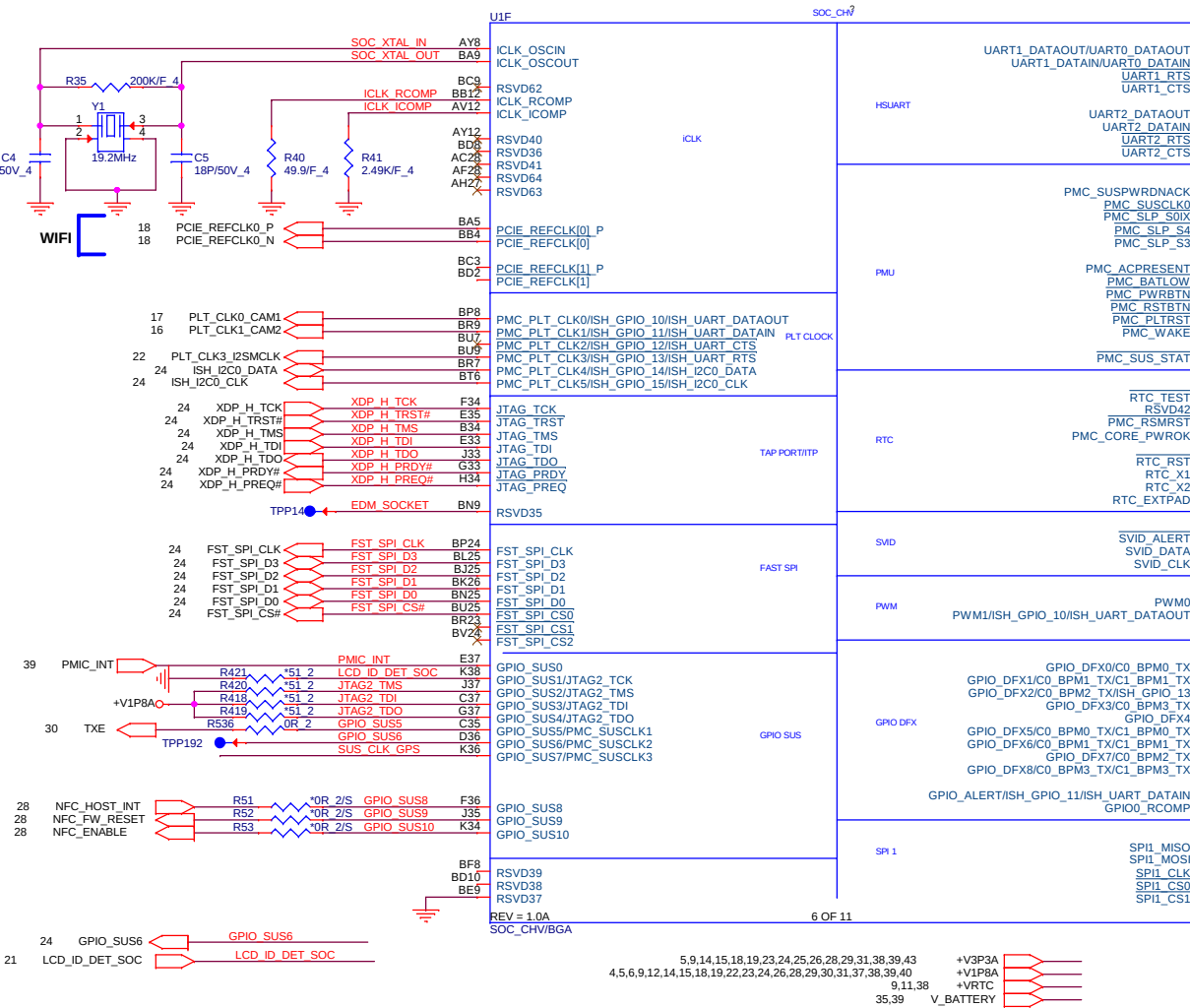
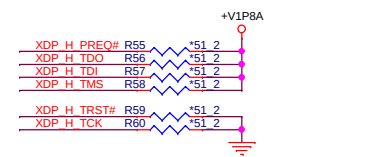
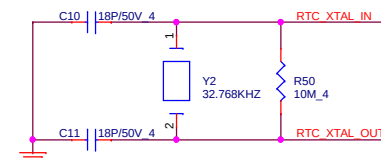


GPIO PU/PD

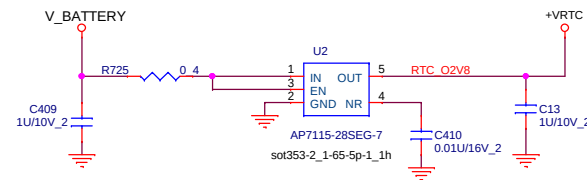


RTC Clock 32.768KHz



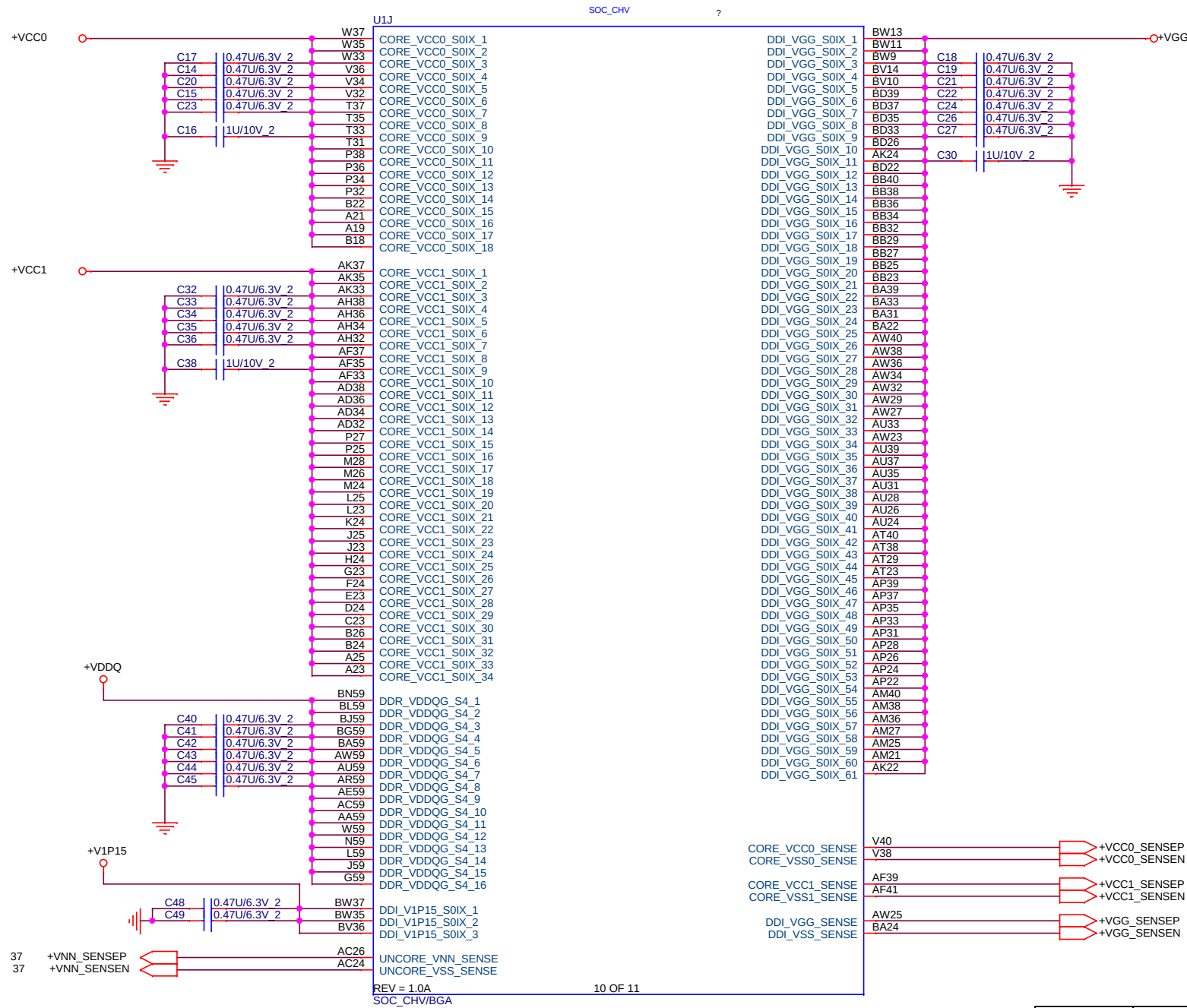
Hardware Strap (EDS1P0)

Pin Name	Purpose	Polarity	Internal PU/PD	
GPIO_SUS0	DDI0 Detected	0 = Port DDI0 is not detected 1 = Port DDI0 is detected	PD	+V1P8A R61 *10K 2 PMIC_INT
GPIO_SUS5	Flash Descriptor Security Override	0 = Override 1 = Normal Operation	PU	R64 *2K 4 GPIO_SUS5
GPIO_SUS7	DFX Sus Debug Strap	0 = SUS Debug 1 = No SUS Debug	PU	+V1P8A R66 *10K 2 R67 2K 4 SUS_CLK_GPS
GPIO_SUS8	SFR Supply Select	0 = Supply is 1.25V 1 = Supply is 1.35V	PD	+V1P8A R68 *10K 2 R69 2K 4 GPIO_SUS8
GPIO_SUS9	ICLK, USB2, DDI SFR Bypass	0 = Use LDOs 1 = Bypass LDOs (supply 1.05V on power pins)	PD	+V1P8A R71 *10K 2 GPIO_SUS9
GPIO_SUS10	POSM Select	0 = Fuse Controller 1 = PMC	PD	+V1P8A R73 *10K 2 GPIO_SUS10



PROJECT : Eagle
Quanta Computer Inc.

Size	Document Number	Rev
	Cherryview-T (CLK/UART/SPI/PMU)	1A
Date:	Wednesday, January 14, 2015	Sheet 7 of 44



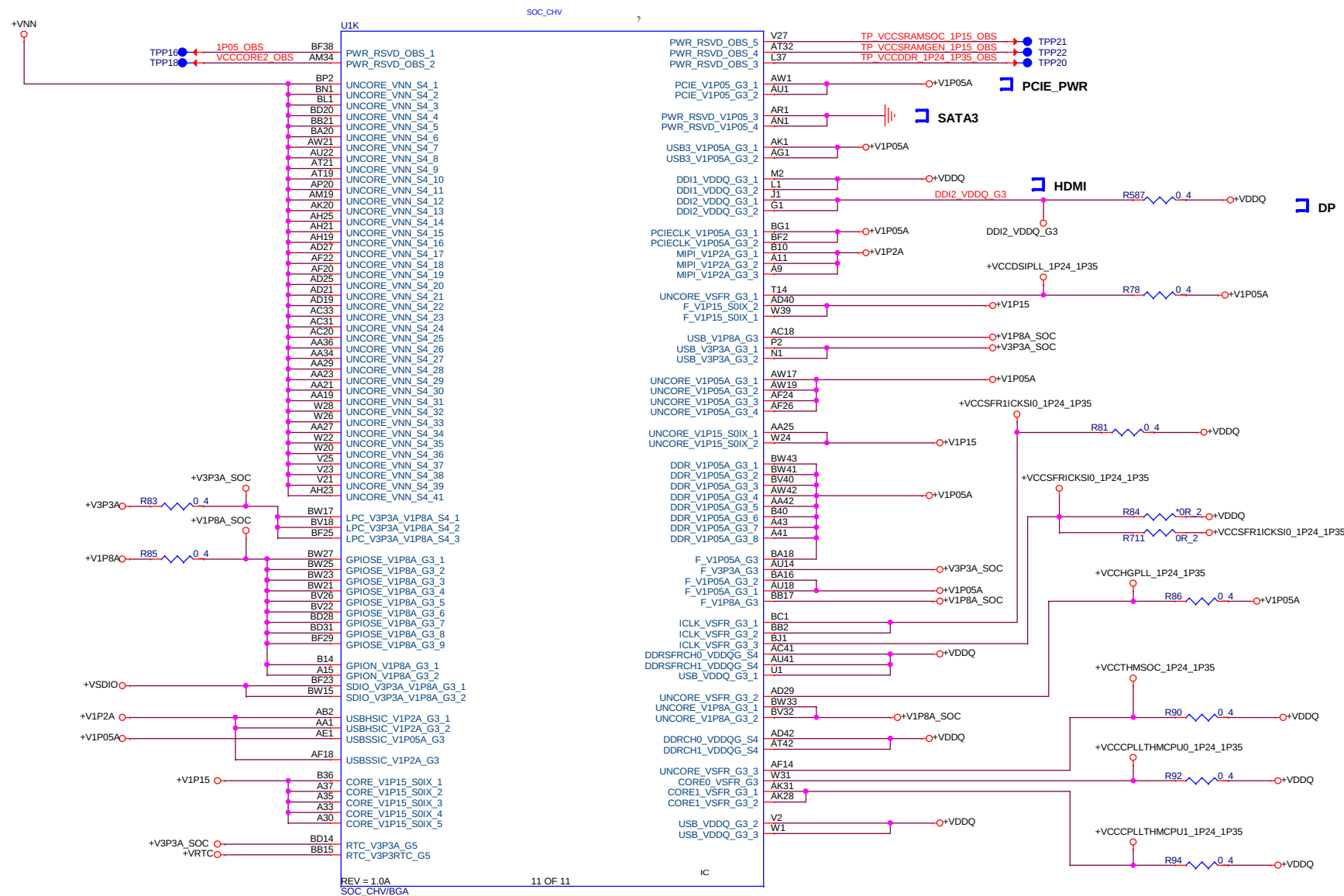
10 OF 11

IC



PROJECT : Eagle
Quanta Computer Inc.
EAGLE

Size	Document Number	Rev
	Cherryview-T (POWER 1/2)	1A
Date:	Wednesday, January 14, 2015	Sheet 8 of 44

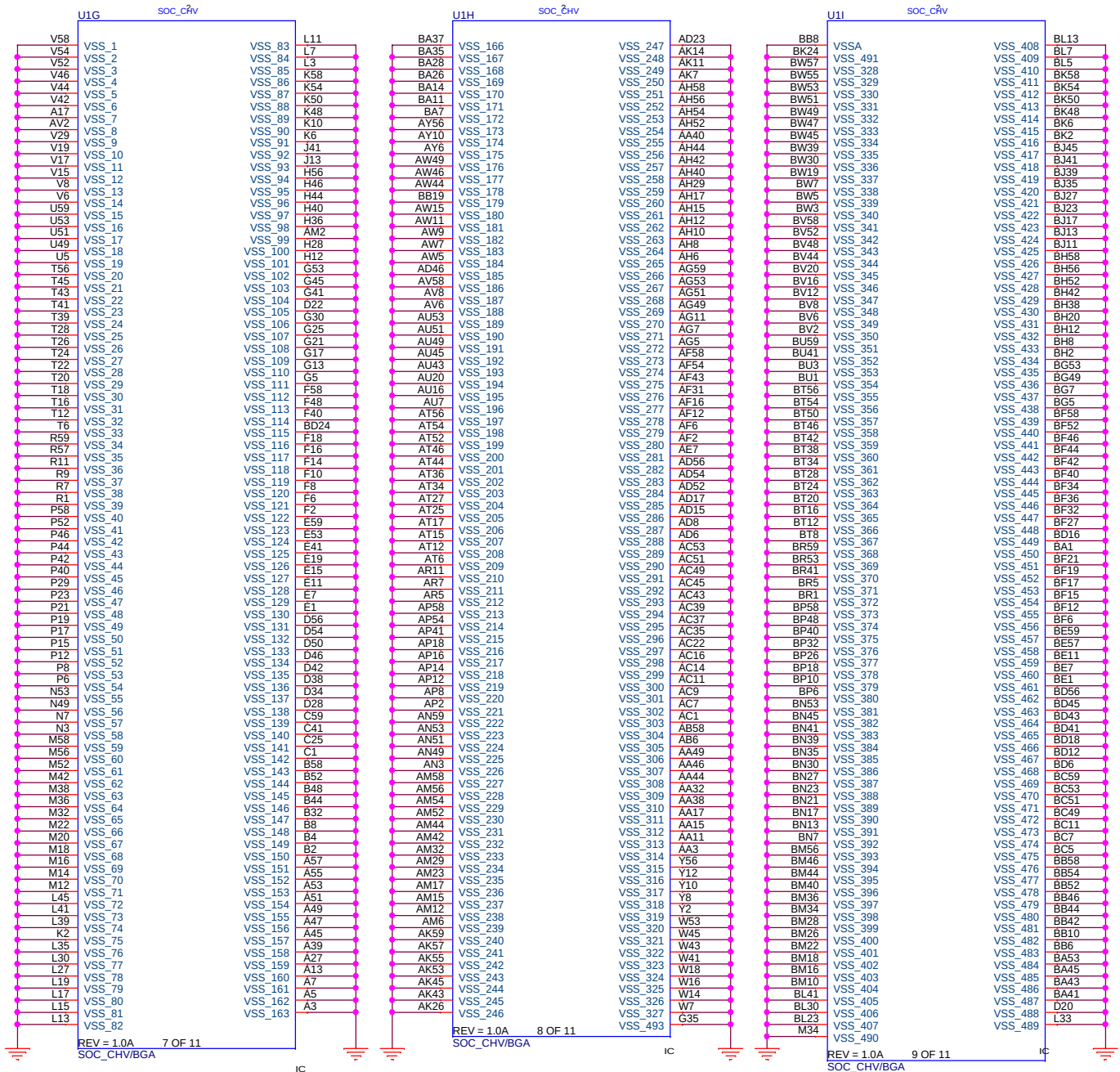


11,37	+VNN
5,14,15,18,19,23,24,25,26,28,29,31,38,39,43	+V3P3A
4,5,6,7,12,14,15,18,19,22,23,24,26,28,29,30,31,37,38,39,40	+V1P8A
11,38	+VSDIO
11,38	+V1P2A
11,37,38	+V1P05A
8,11,24,37	+V1P15
7,11,38	+VRTC
8,11,12,13,37,38	+VDDQ



PROJECT : Eagle
Quanta Computer Inc.

Size	Document Number	Rev
	Cherryview-T (POWER 2/2)	1A
Date:	Wednesday, January 14, 2015	Sheet 9 of 44

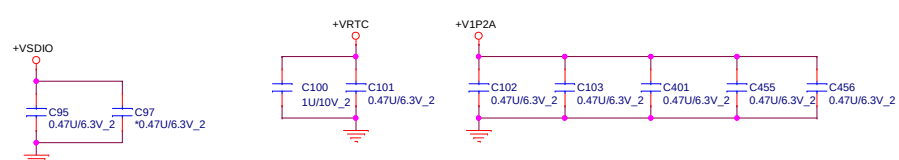
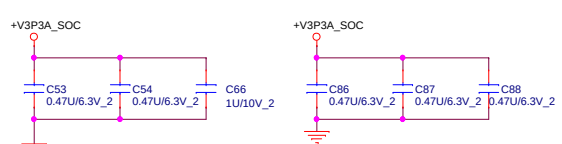
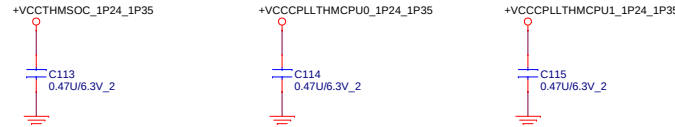
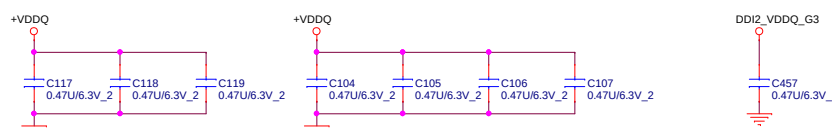
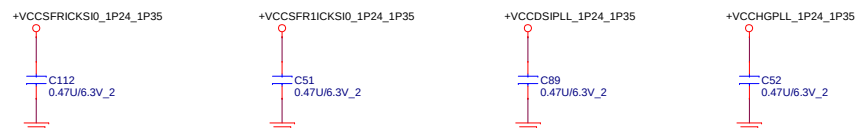
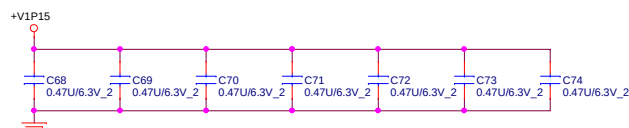
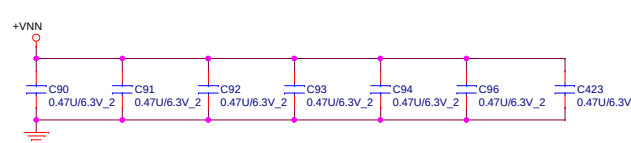
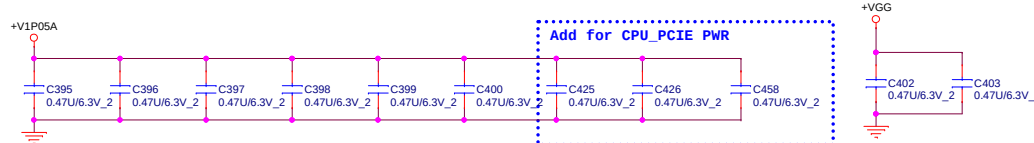
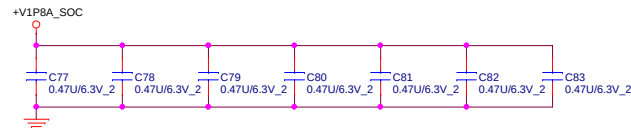
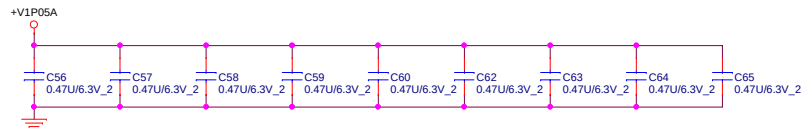


QBCON	TOPB/S	Vender PN
AJ0QH7BUT01	AJ0QH7BUT00	2.0G QH7B
AJ0QH7CUT01	AJ0QH7CUT00	2.0G QH7C
AJ0QHAUUT01	AJ0QHAUUT00	2.16G QHAU

NB5/HW

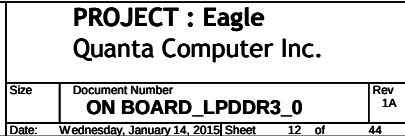
PROJECT : Eagle
Quanta Computer Inc.

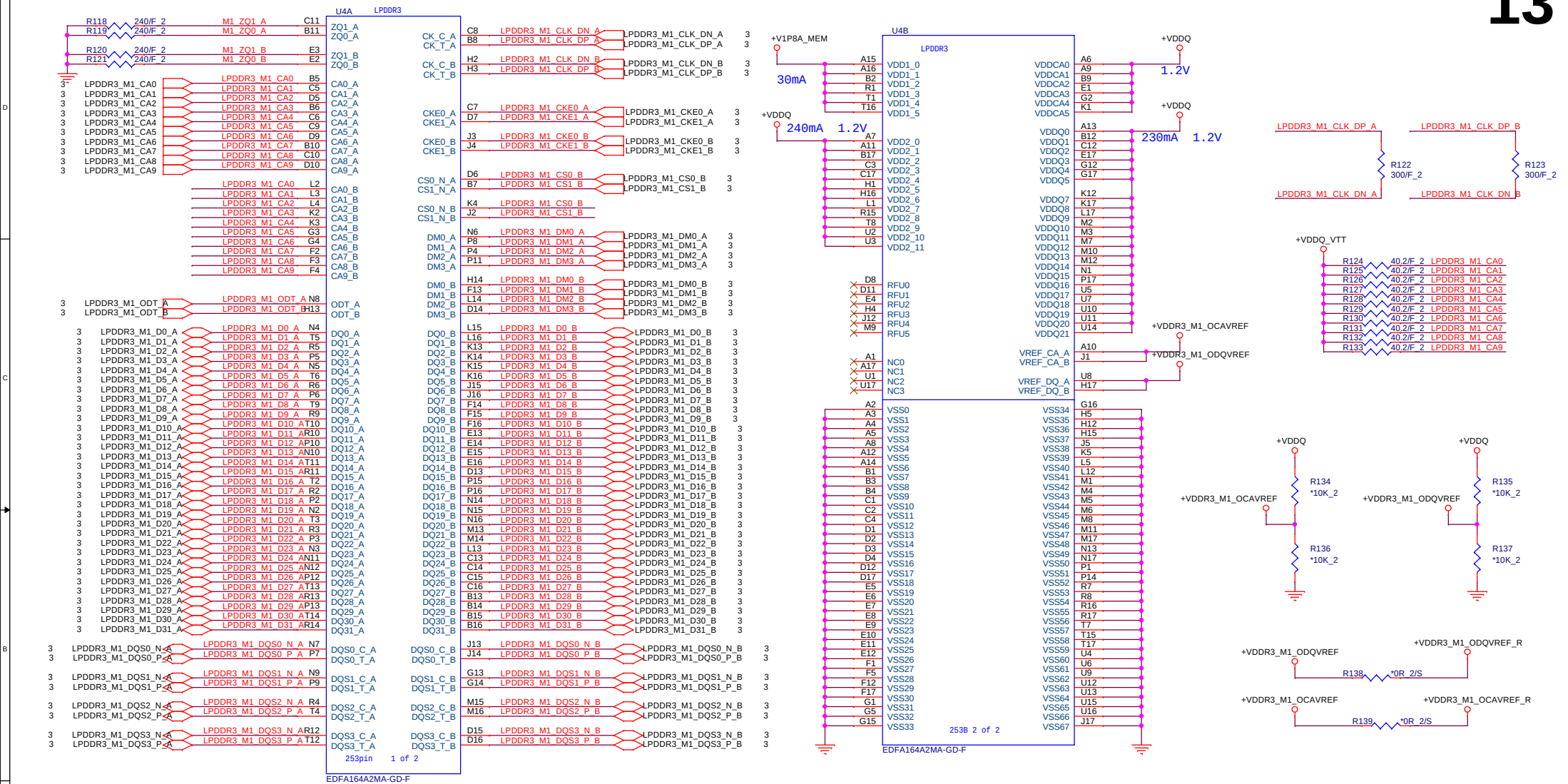
Size	Document Number	Rev
	Valleyview-T (GND)	1A
Date: Wednesday, January 14, 2015 Sheet 10 of 44		



9,37,38	+V1P05A
8,9,24,37	+V1P15
8,9,12,13,37,38	+VDDQ
9	+V1P8A_SOC
9	+V3P3A_SOC
9,38	+V1P2A
7,9,38	+VRTC
9	+VCCSFR1ICKSI0_1P24_1P35
9	+VCCDSIPLL_1P24_1P35
9	+VCCTHMSOC_1P24_1P35
9	+VCCCPLLTHMCPU0_1P24_1P35
9	+VCCCPLLTHMCPU1_1P24_1P35
9	+VCCHGPLL_1P24_1P35

 NB5/HW	PROJECT : Eagle Quanta Computer Inc.	
	Size	Document Number
	Valleyview-T (GND)	
Date:	Wednesday, January 14, 2015	Sheet 11 of 44





8Gb /16Gb 1600MHz 1.2v LPDDR3 128Mx32x2 & 128Mx32x4 FBGA253 Memory IC

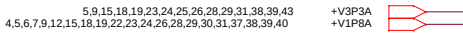
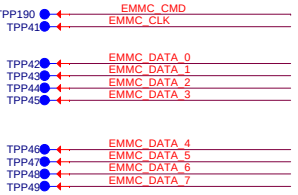
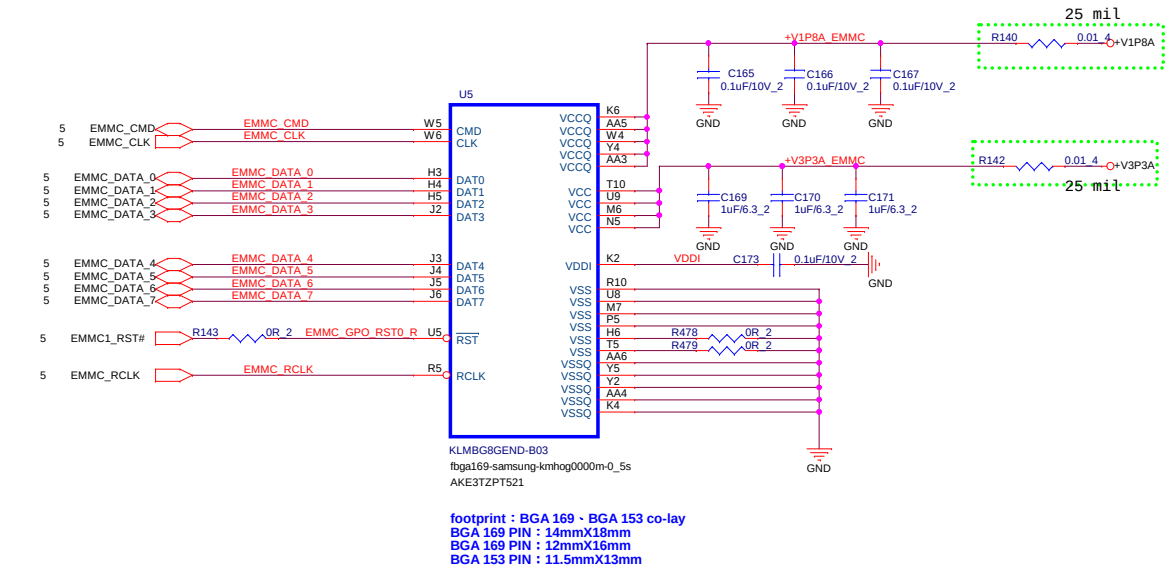
QBCON	TOPB/S	Vender PN	SIZE	
AKD5QYGTLO1	AKD5QYGTLO0	EDF8164A3MA-GD-F	8Gb	Elpida
AKE3TZPT516	AKE3TZPT515	EDFA164A2MA-GD-F	16Gb	Elpida
AKD5QWSTW02	AKD5QWSTW01	H9CCNNN8KTALBR-NTD	8Gb	Hynix
AKD5RWSTW08	AKD5RWSTW07	H9CCNNNBPTALBR-NTD	16Gb	Hynix
AKD5PWS7508	AKD5PWS7507	K3QF1F10DM-AGCE	8Gb	Samsung
AKD5RWST514	AKD5RWST513	K3QF2F20DM-AGCE	16Gb	Samsung



PROJECT : Eagle
Quanta Computer Inc.

Size	Document Number	Rev
	ON BOARD_LPDDR3_1	1A
Date:	Wednesday, January 14, 2015	Sheet 13 of 44

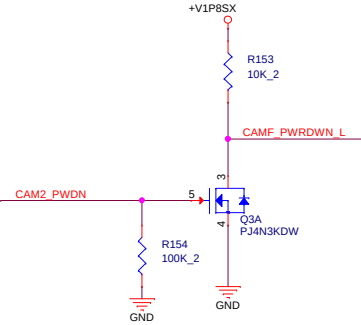
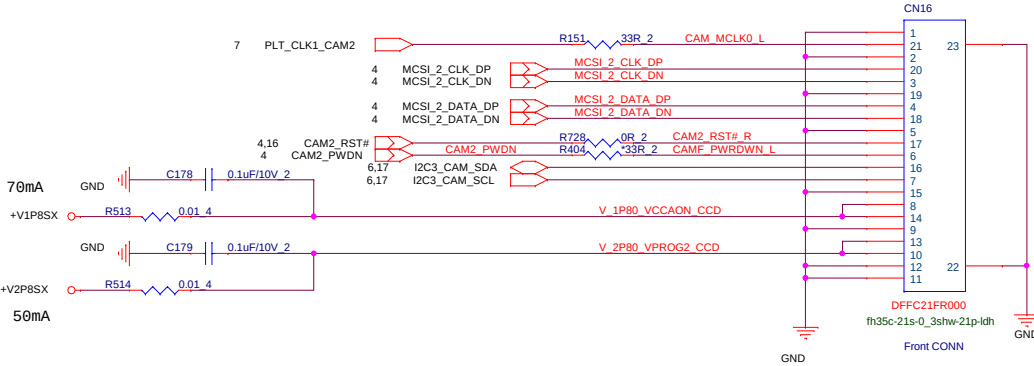
EMMC



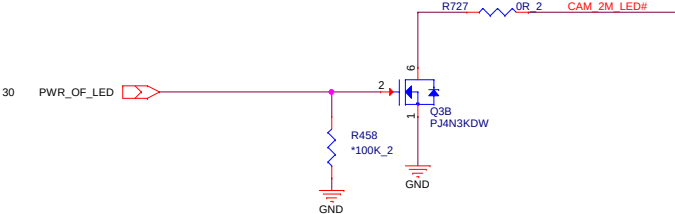
iNAND (eMMC 5.0)				
QBCON	TOPBIS	Vender PN	SIZE	
AKE5SZ0T507	AKE5SZ0T506	KLMBG4GEAC-B031	32G	Samsung
AKE3TZPT516	AKE3TZPT515	KLMBG8GEAC-B031	64G	Samsung
AKE3SZ-TW02	AKE3SZ-TW01	H26M64103EMR	32G	Hynix
AKE3TG-TW02	AKE3TG-TW01	H26M78103CCR	64G	Hynix
AKE3UFPT103	AKE3UFPT102	SDIN8CE4-128G	128G	Sandisk
AKE3SFUT001	AKE3SFUT000	SDIN9DW4-32G	32G	Sandisk
AKE3TFUT102	AKE3TFUT101	SDIN9DW4-64G	64G	Sandisk



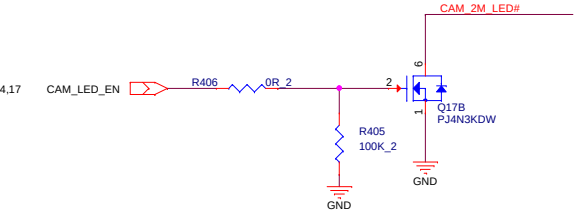
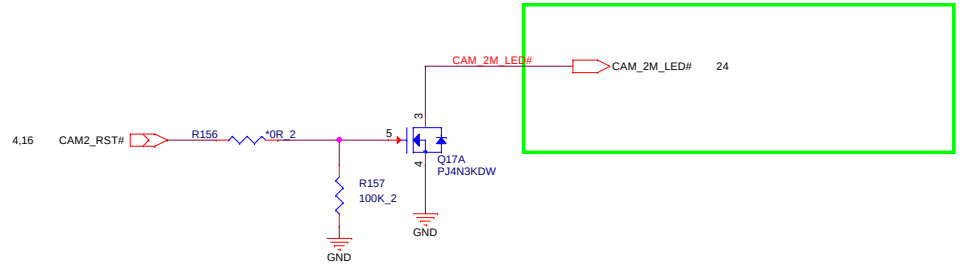
Front Camera Module Connector



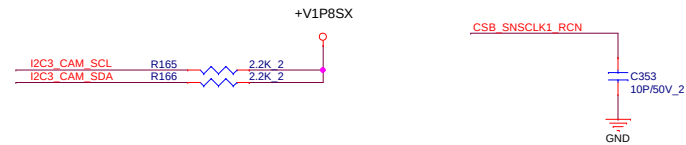
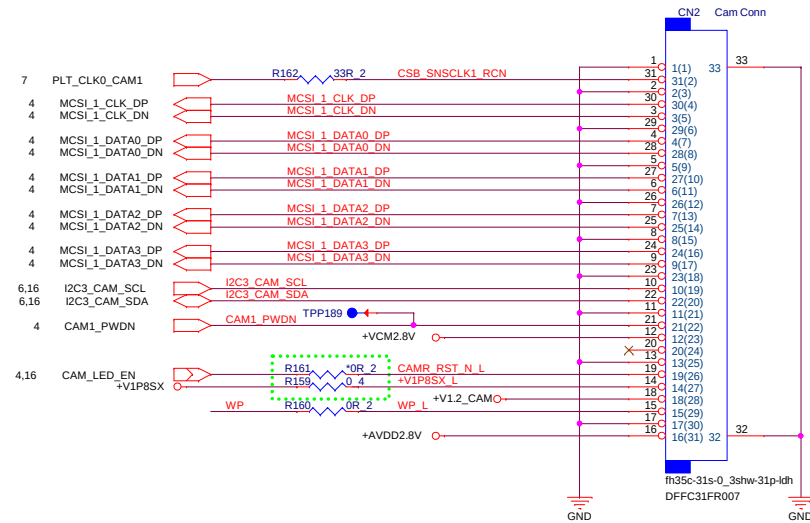
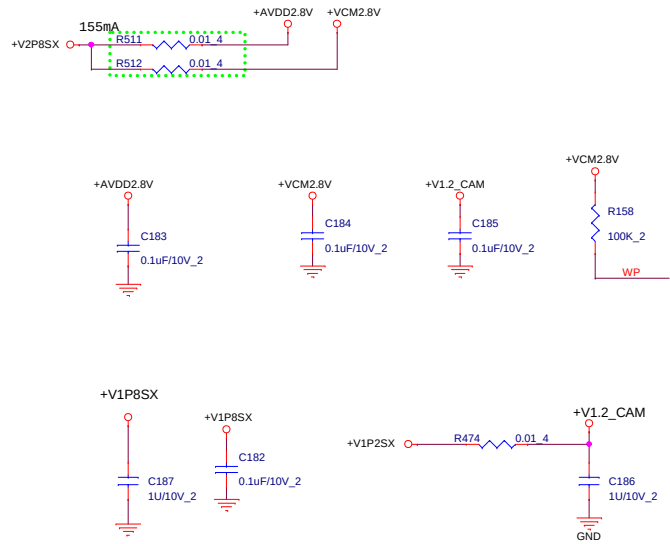
CAM_1_PWRDWN_L High --> power down



Camera 2M LED control



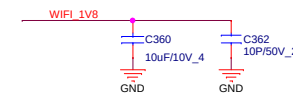
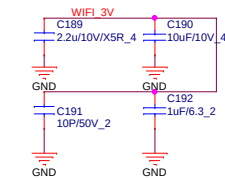
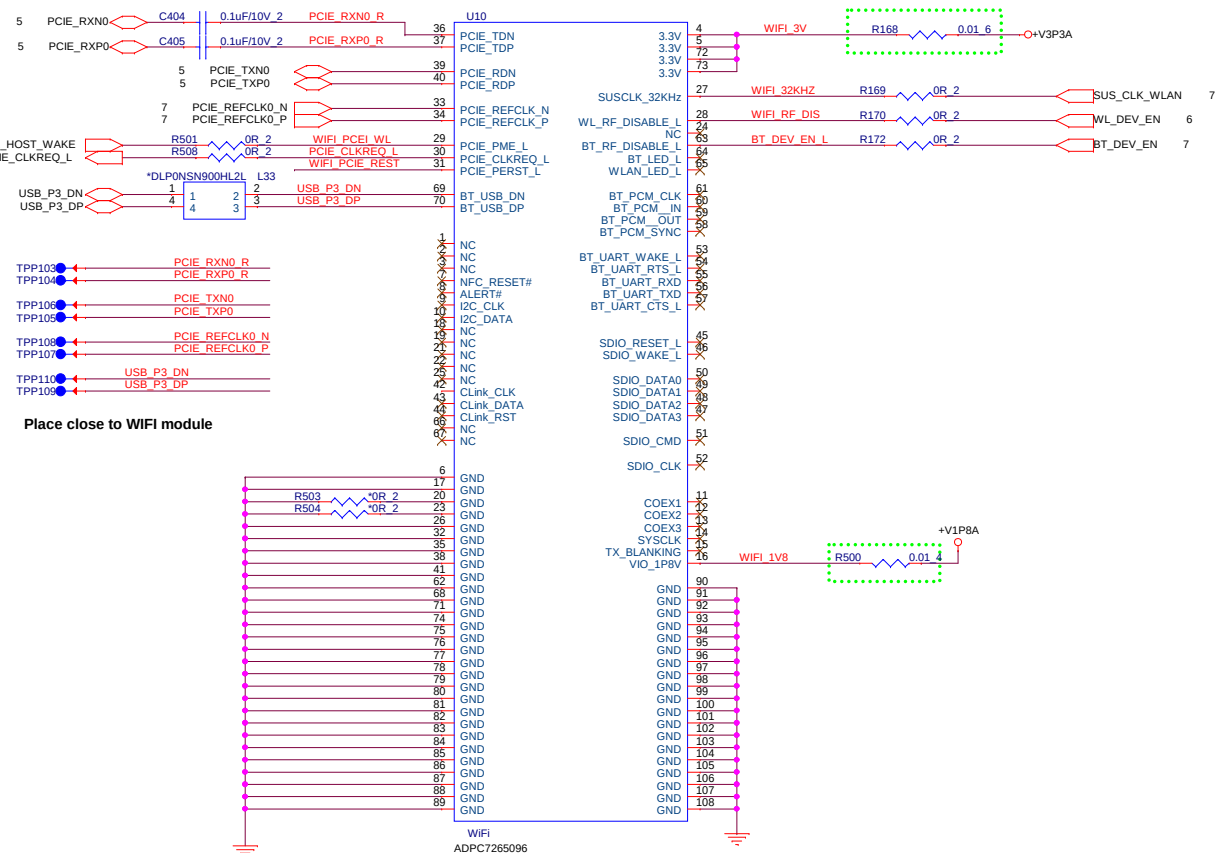
WEBCAM LED + CCD_8M CONNECTOR



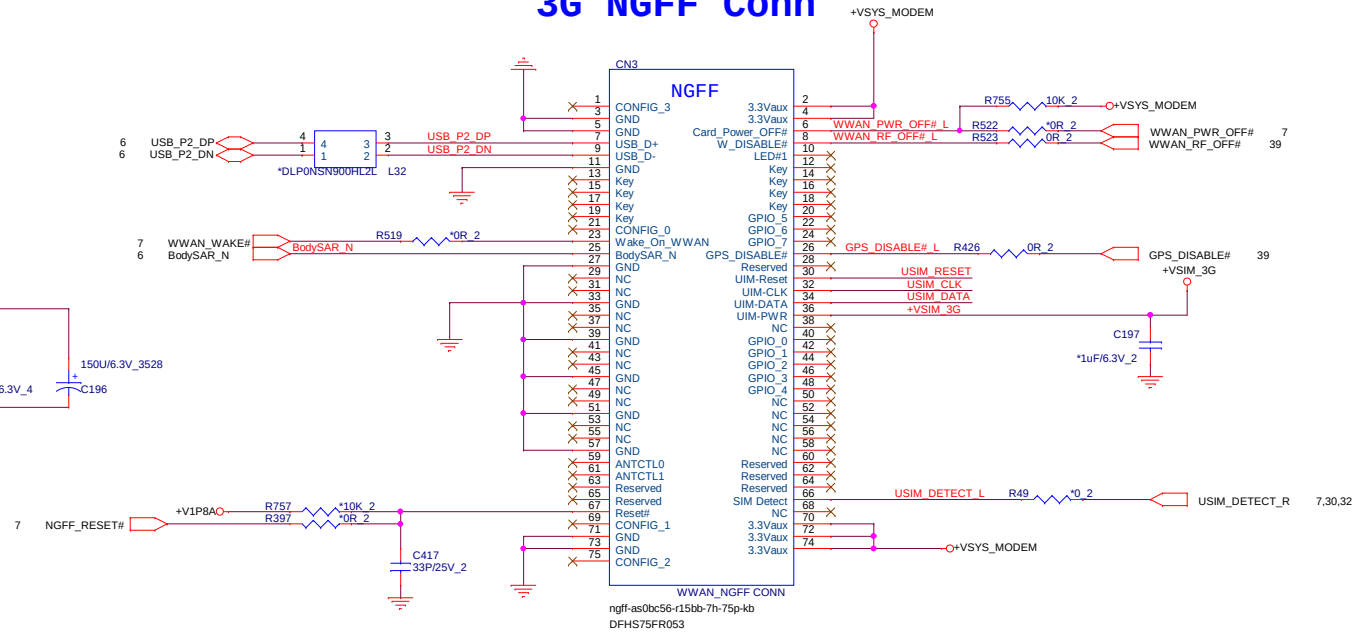
16.38 +V1P8SX
16.38 +V2P8SX
5,9,14,15,18,19,23,24,25,26,28,29,31,38,39,43 +V3P3A

5,9,14,15,19,23,24,25,26,28,29,31,38,39,43

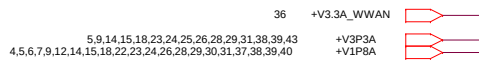
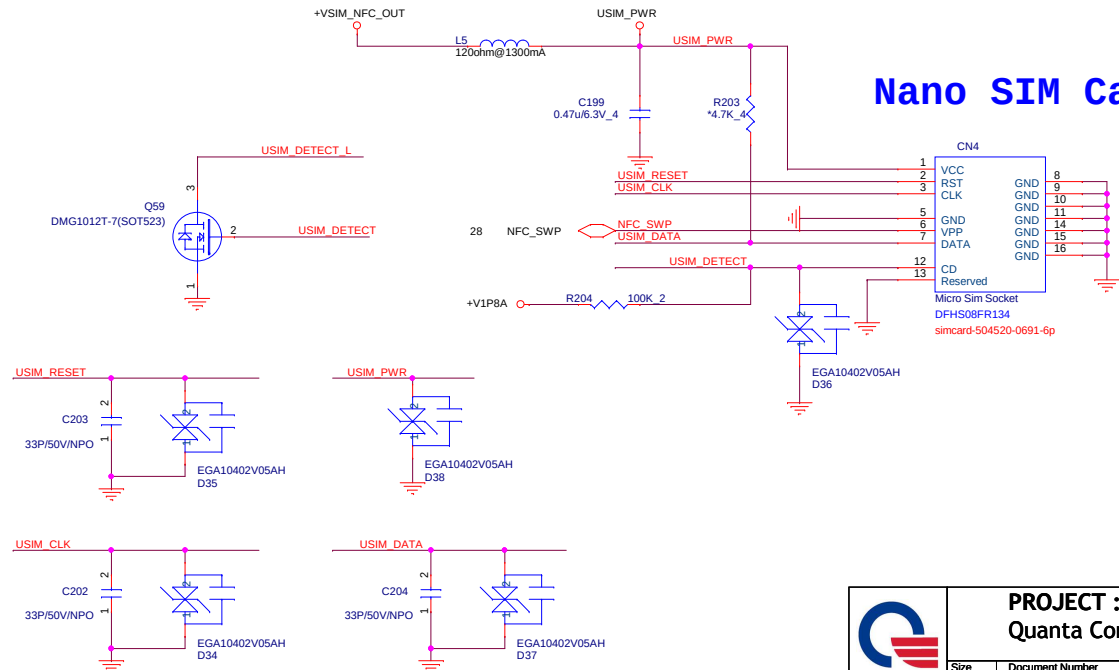
+V3P3A



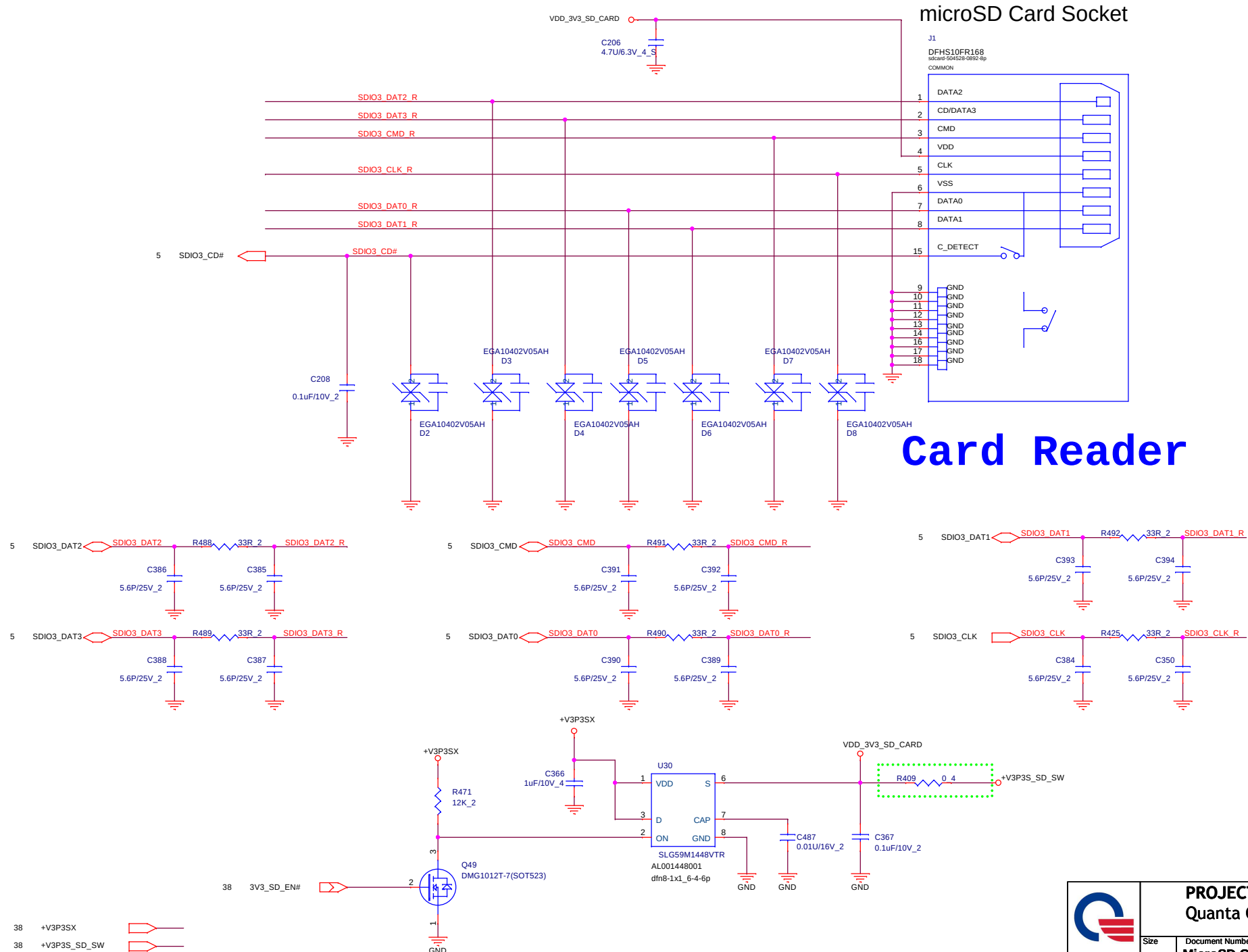
3G NGFF Conn



Nano SIM Card

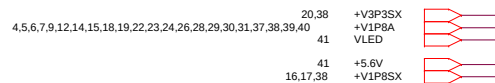
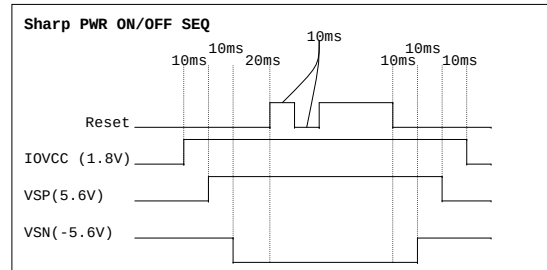
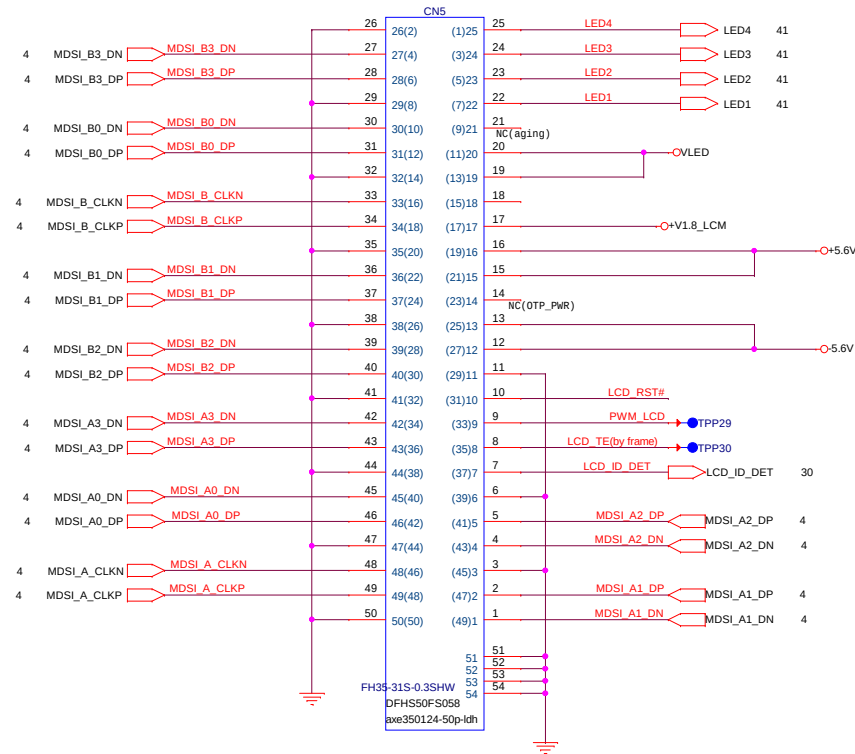
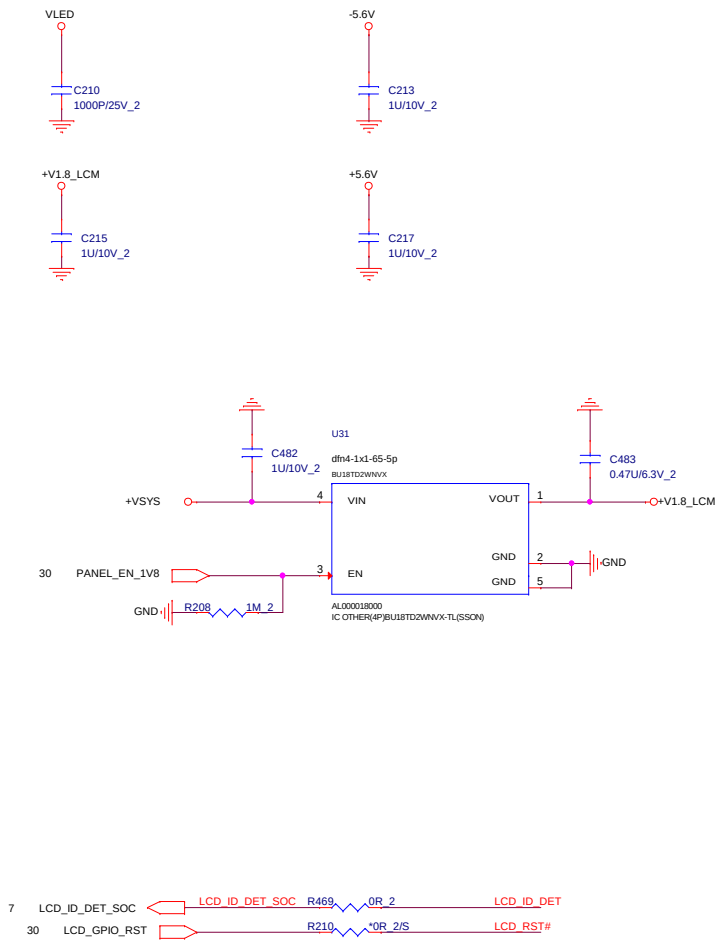


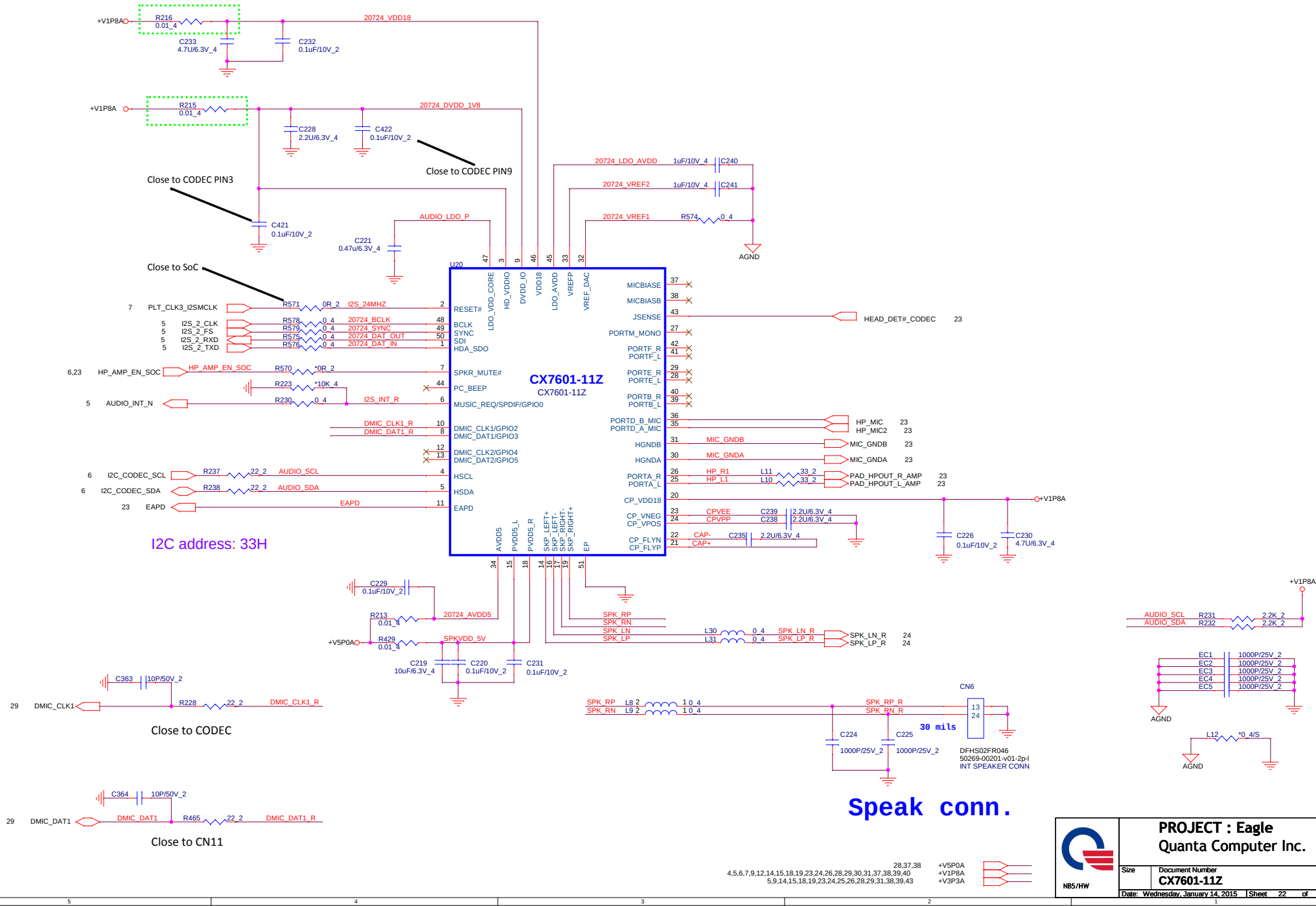
Place close to J3



PROJECT : Eagle
Quanta Computer Inc.

Size	Document Number	Rev
	MicroSD Card	1A
Date: Wednesday, January 14, 2015	Sheet 20 of 44	

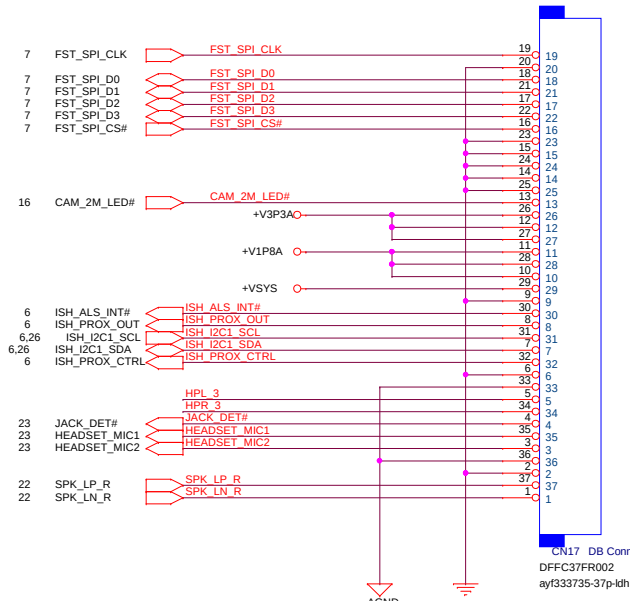
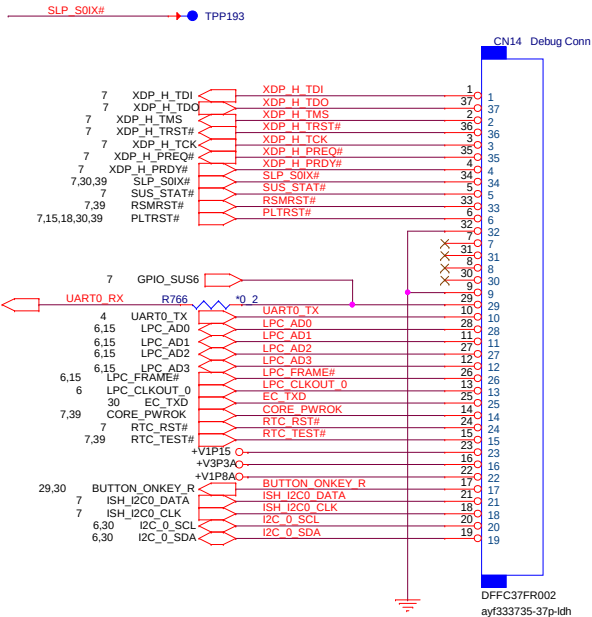




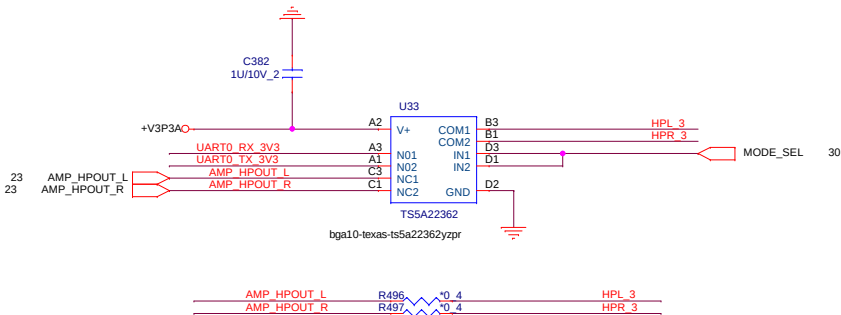
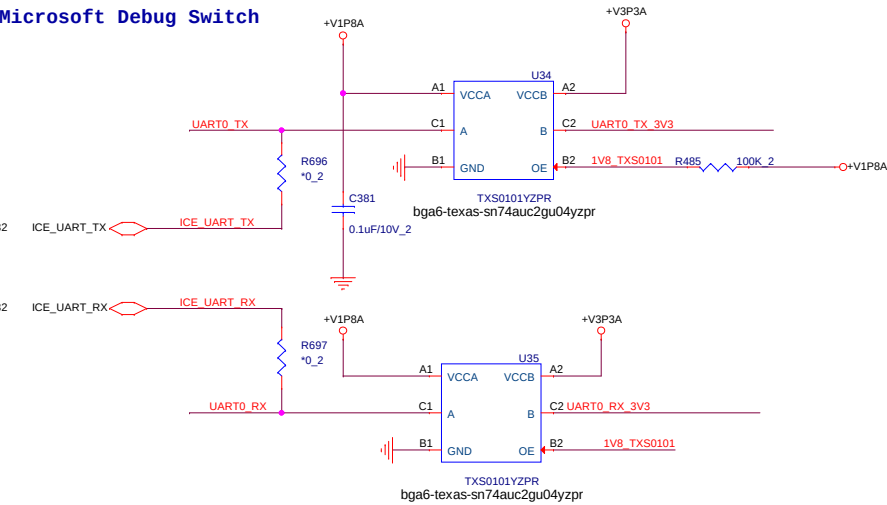


Daughter Board / Debug Con

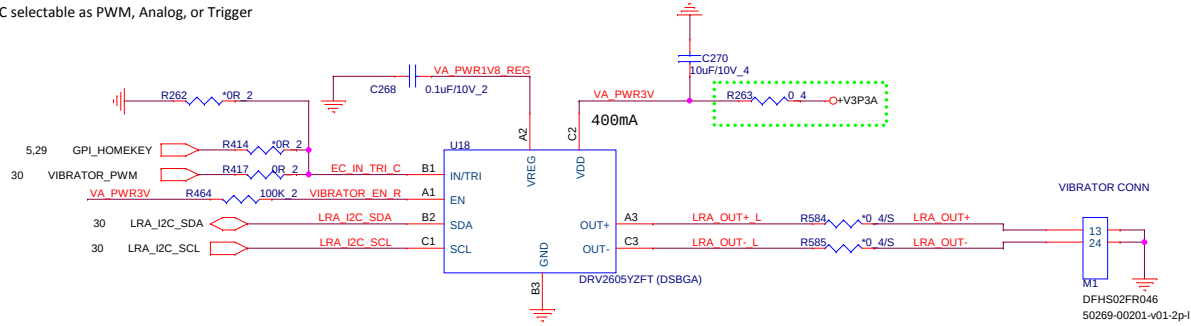
Intel XTP
80 Port Debug
SoC UART
Debug



Microsoft Debug Switch



Multi-mode Input. I2C selectable as PWM, Analog, or Trigger



4,5,6,7,9,12,14,15,18,19,22,23,24,26,28,29,30,31,37,38,39,40
5,9,14,15,18,19,23,24,26,28,29,31,38,39,43

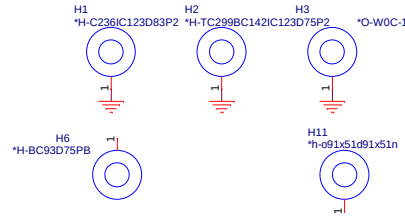
+V1P8A
+V3P3A

A vertical bar is divided into four segments labeled A, B, C, and D from bottom to top. An arrow points to the boundary between segments B and C.

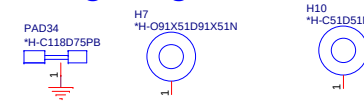


Top Shielding Can using

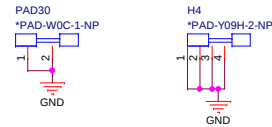
Hole



Aligning hole



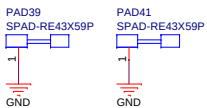
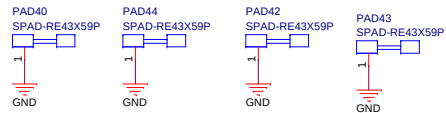
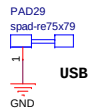
BOT SHIELDING PAD



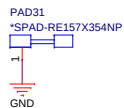
TOP SHIELDING PAD



ME WIRE PAD



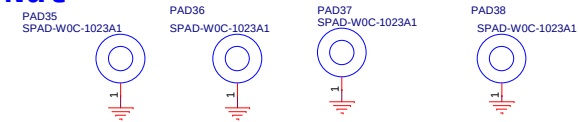
TS GND PAD



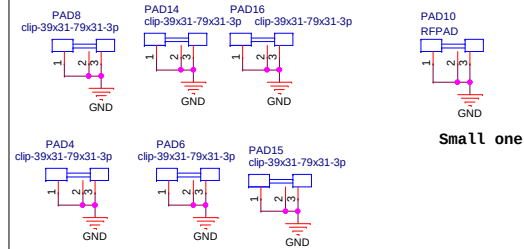
USB3 SHIELDING



Nut

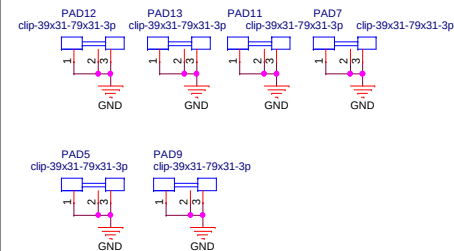


BOT shielding: Up



Small one

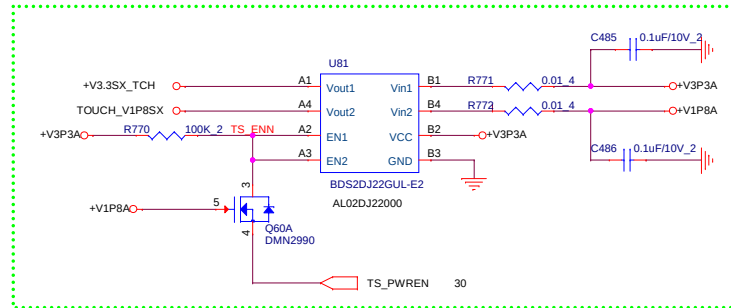
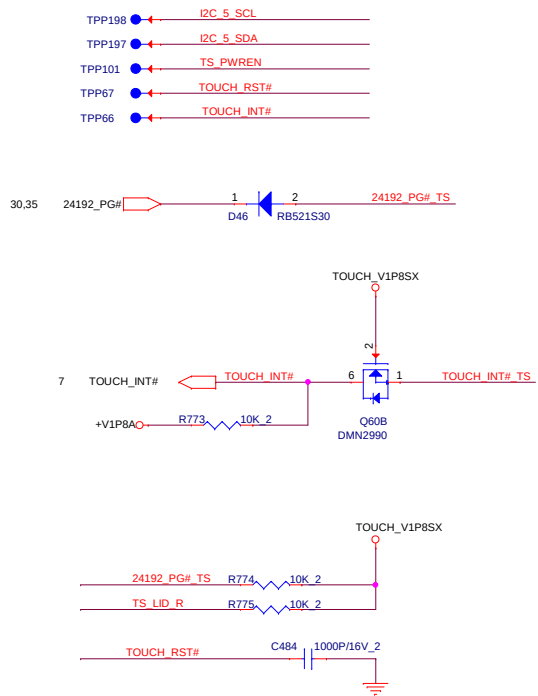
BOT shielding: Down



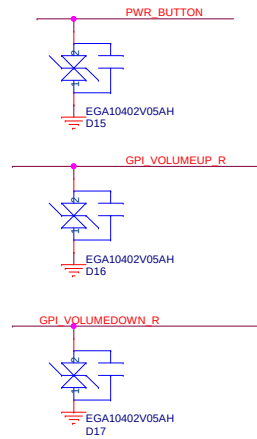
PROJECT : Eagle
Quanta Computer Inc.

Size	Document Number	Rev
Custom	RFPAD	1A
Date: Wednesday, January 14, 2015	Sheet 27 of 44	

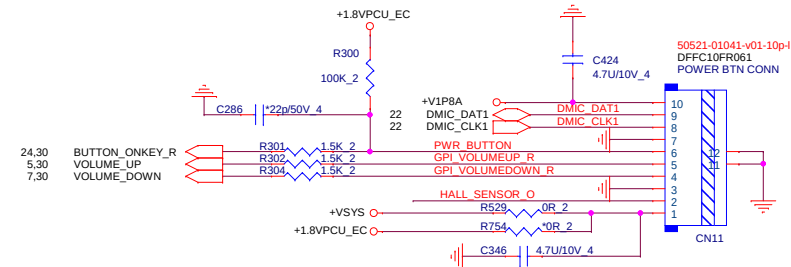
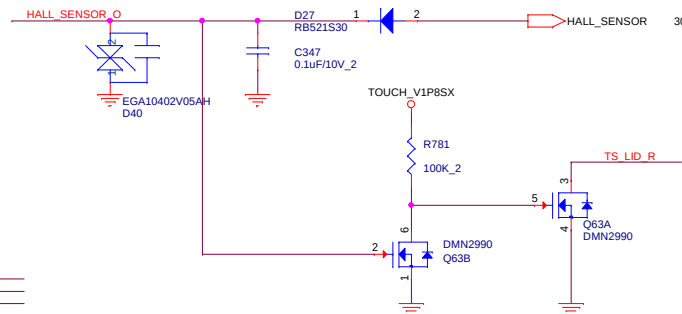
 NBS/HW	PROJECT : Eagle Quanta Computer Inc.		
	Size	Document Number	Rev
		NFC - EA	1A
Date: Wednesday, January 14, 2015		Sheet	28 of 44



SLP_S0IX#	PANEL_EN_EC	TS_PWREN	Notes
0	0	0	TS_LID's behavior is as AND gate with SLP_S0IX# & PANEL_EN_EC
0	1	X	X
1	0	X	X
1	1	1	TS_LID's behavior is as AND gate with SLP_S0IX# & PANEL_EN_EC

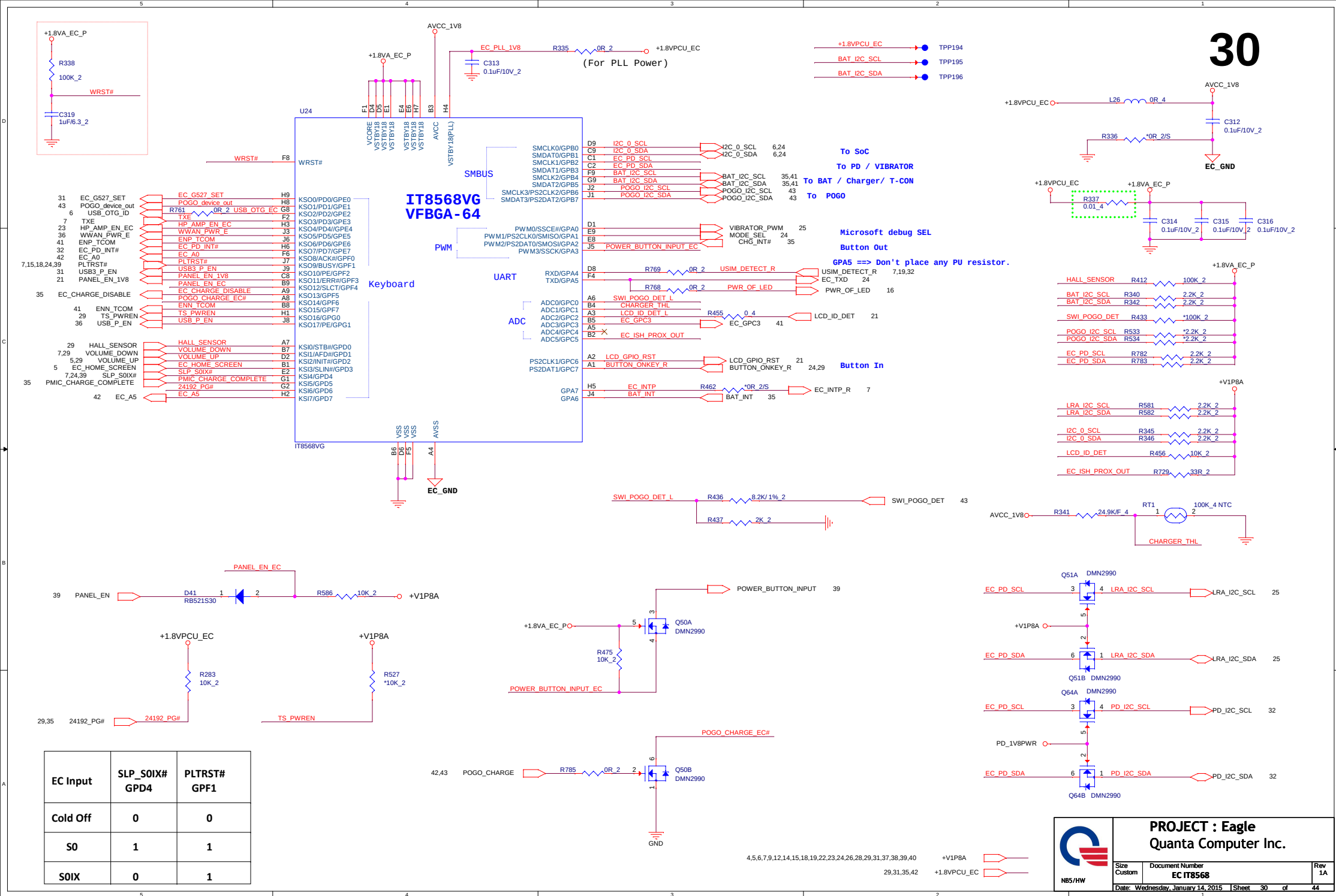


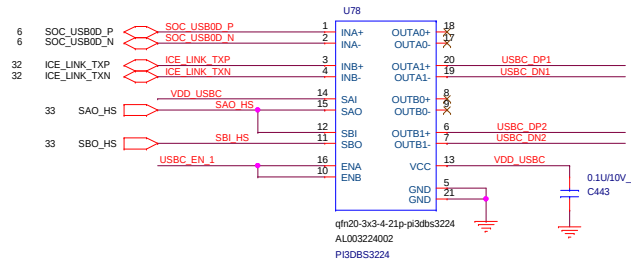
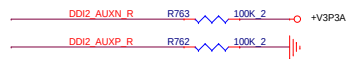
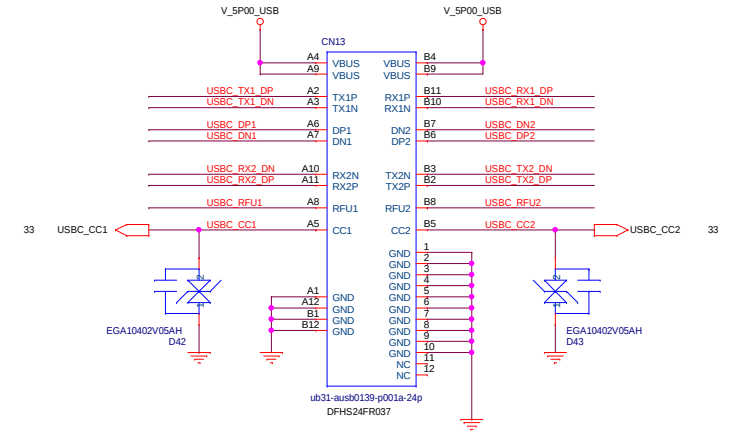
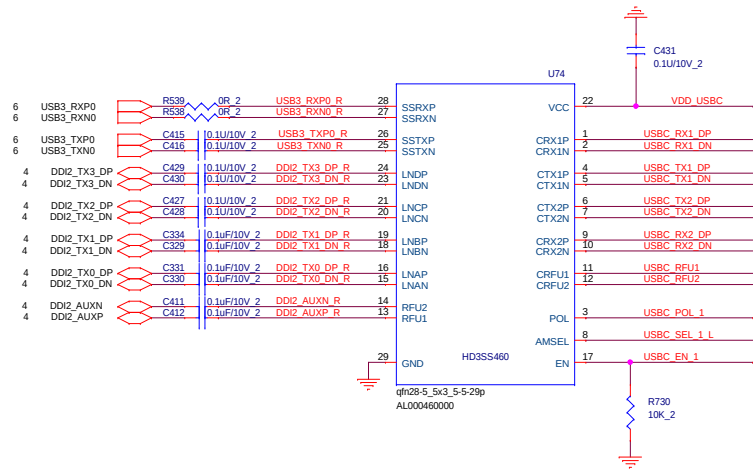
Hall Sensor



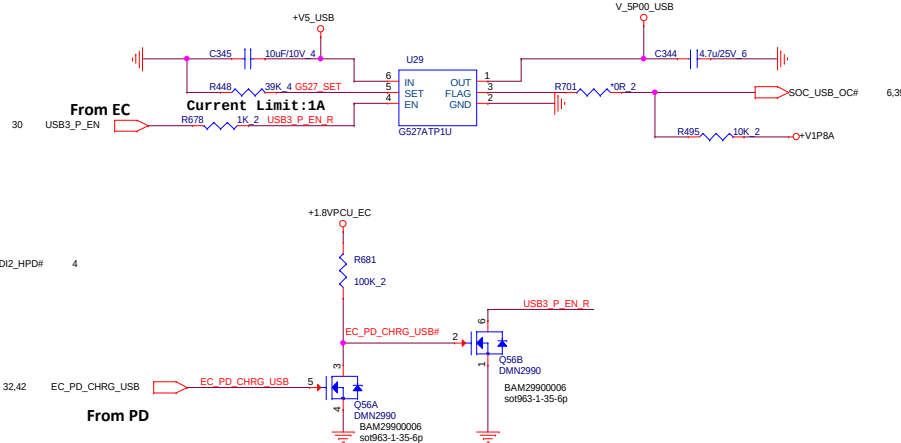
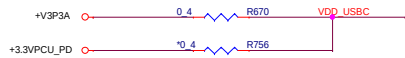
PWR & Vol Conn

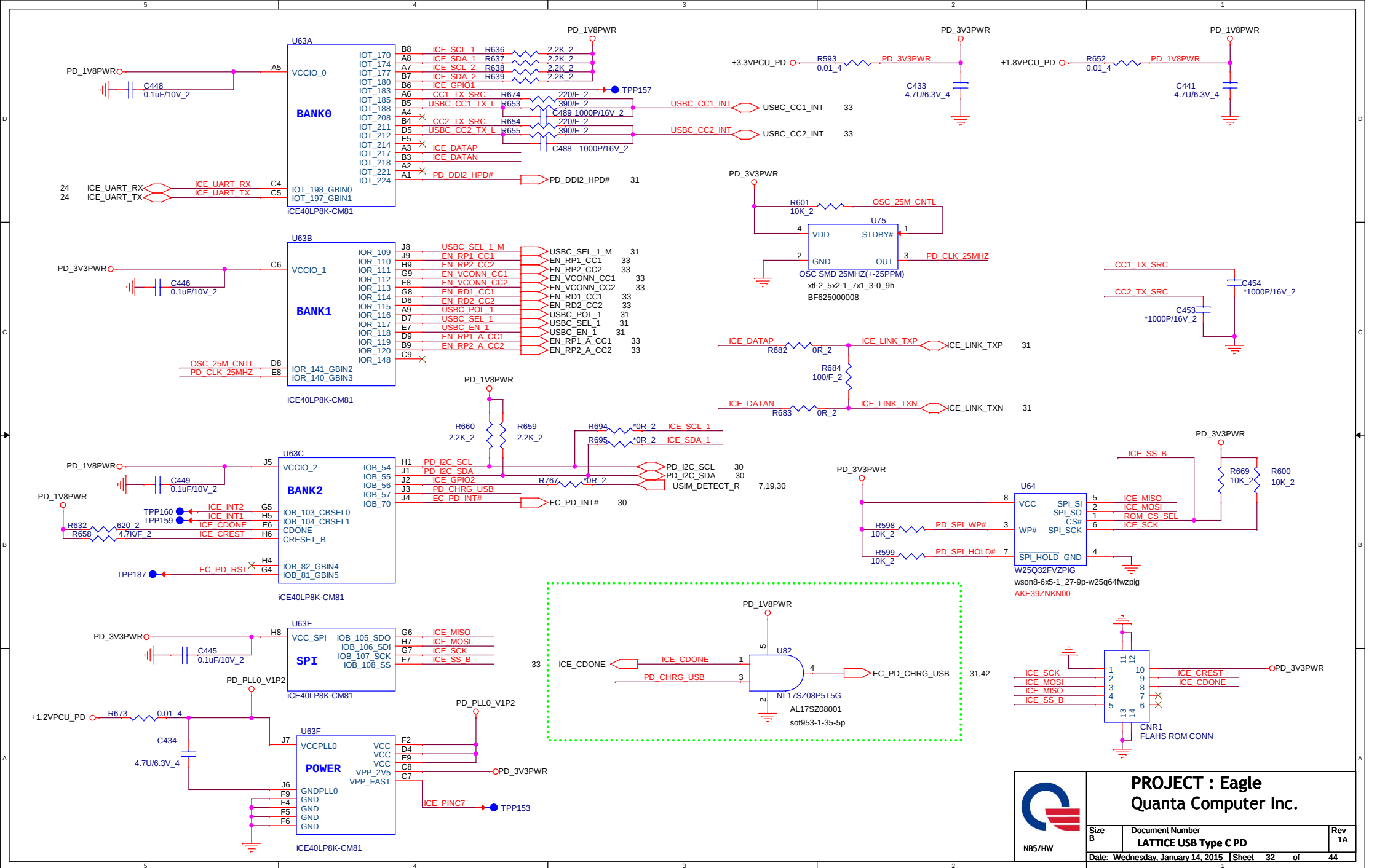


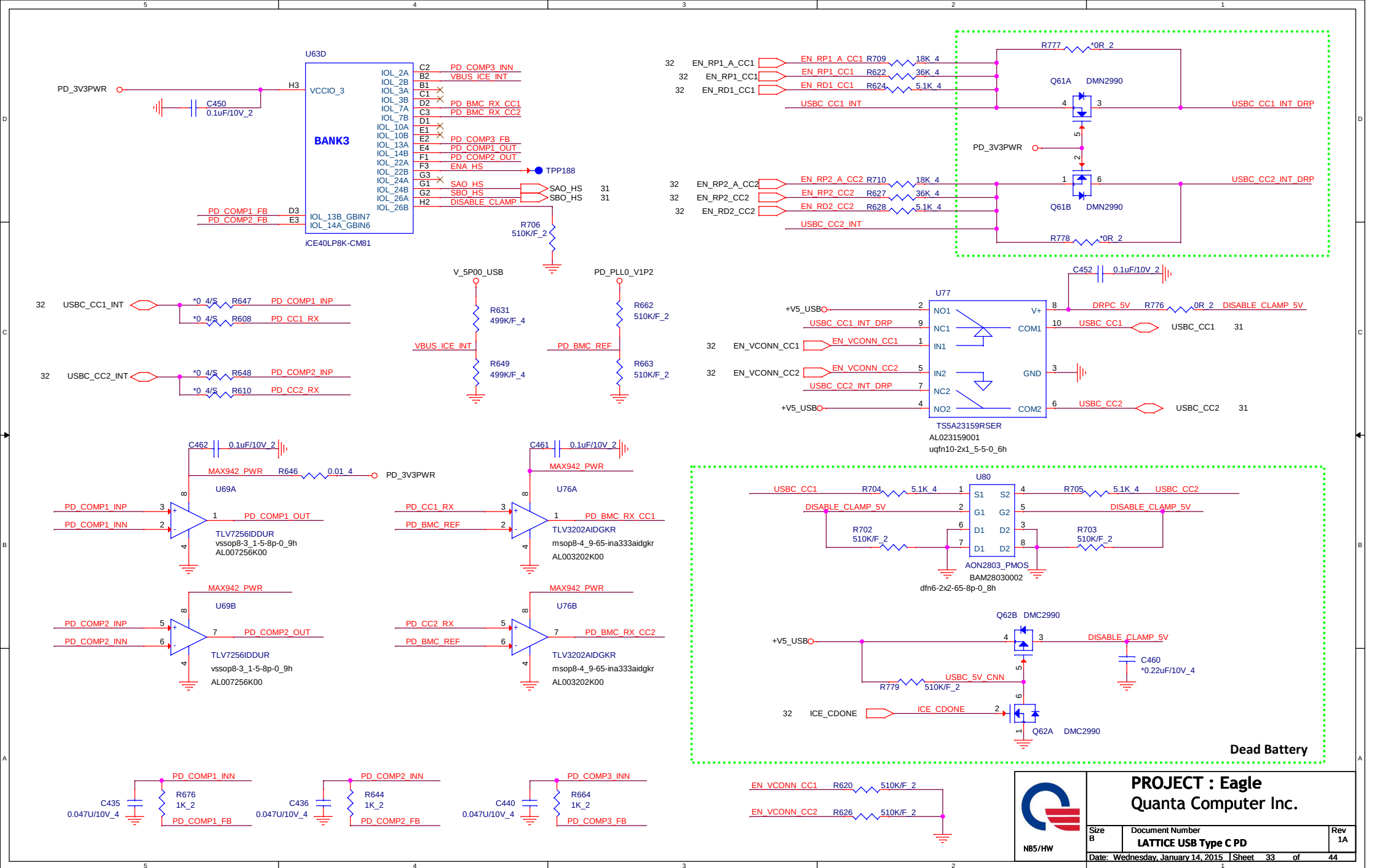




SAI/SAO/SBI/SBO	OUTA1	OUTB1
1111	INA	INB
1110	INA	
1001		INA
ENA/ENB	OUTA0/1	OUTB0/1
11	-----	-----

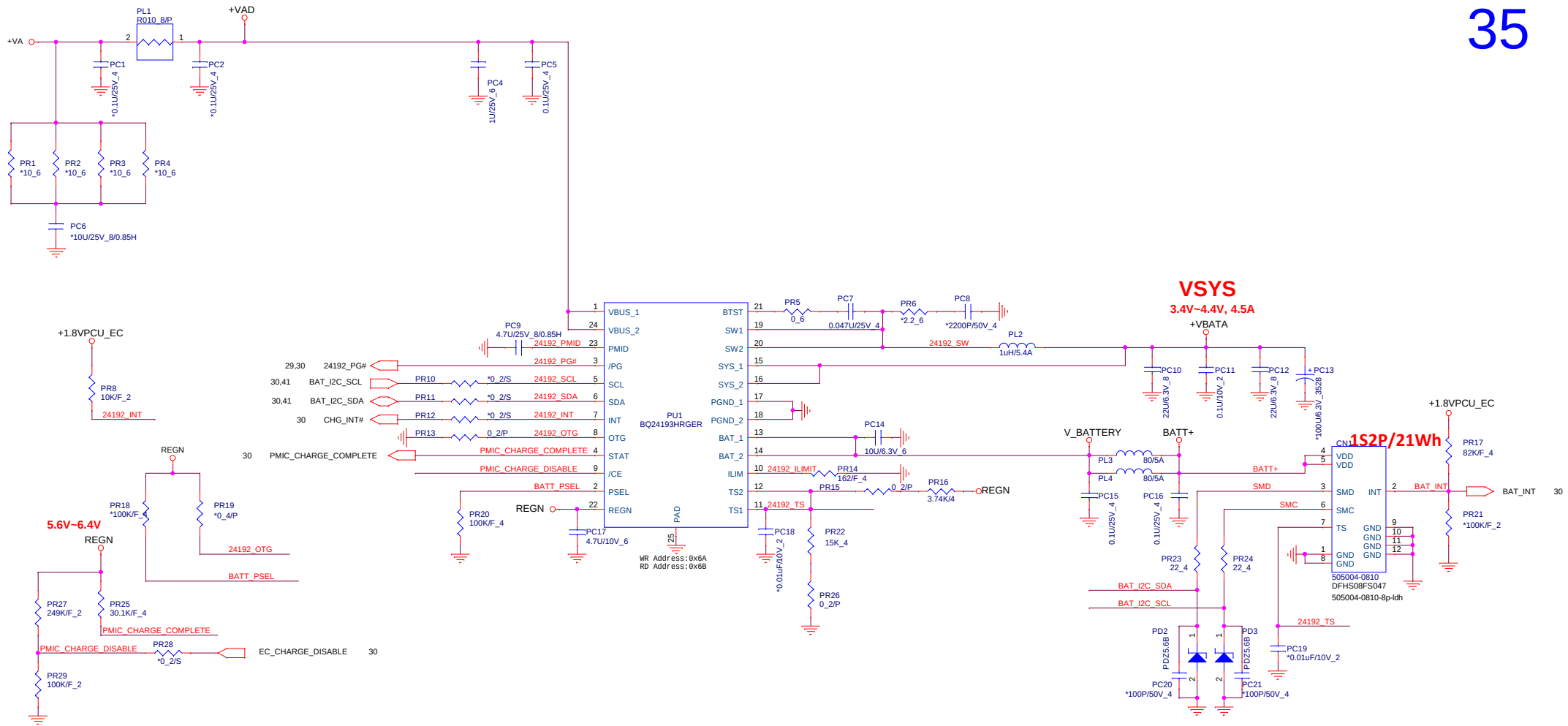


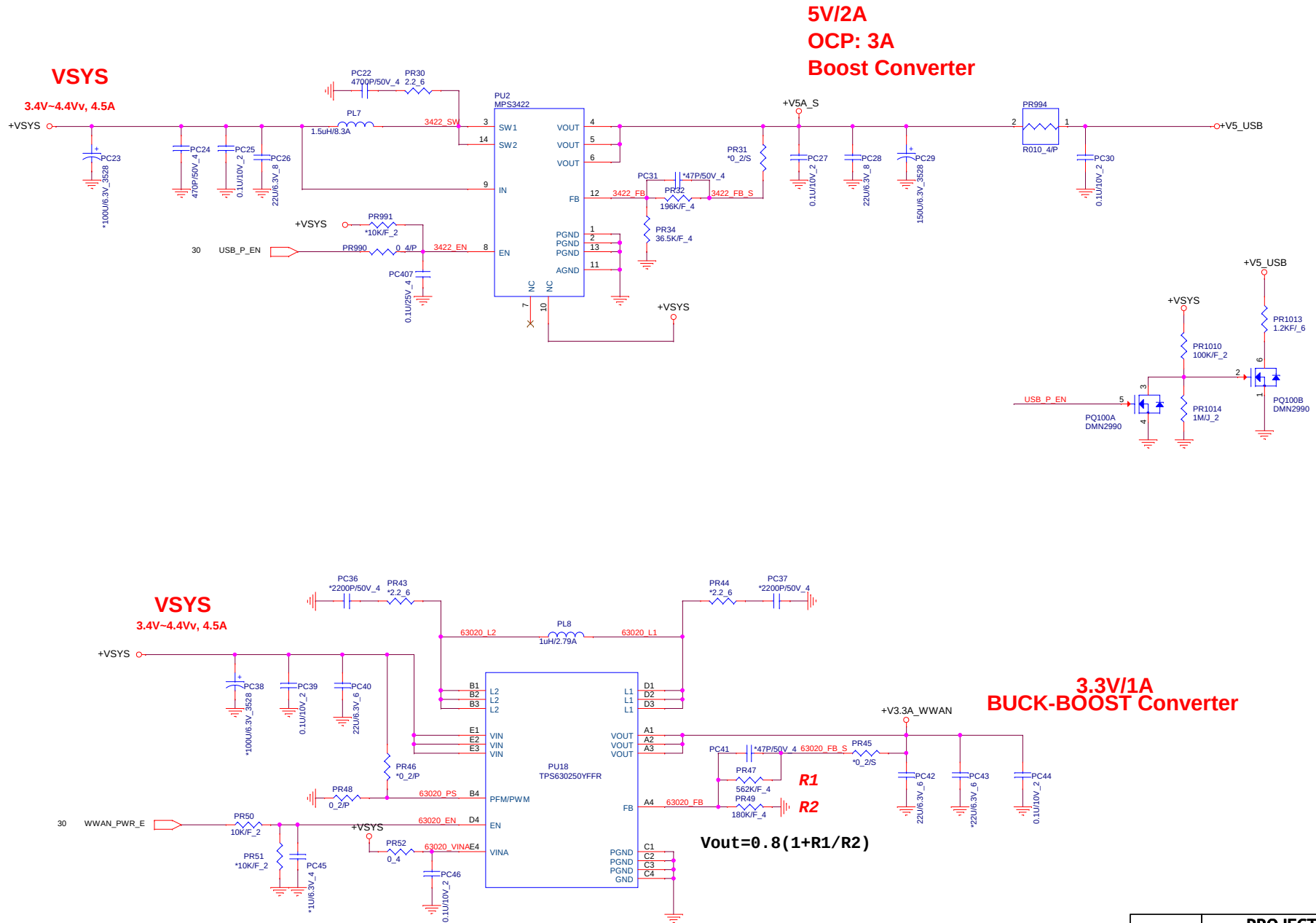


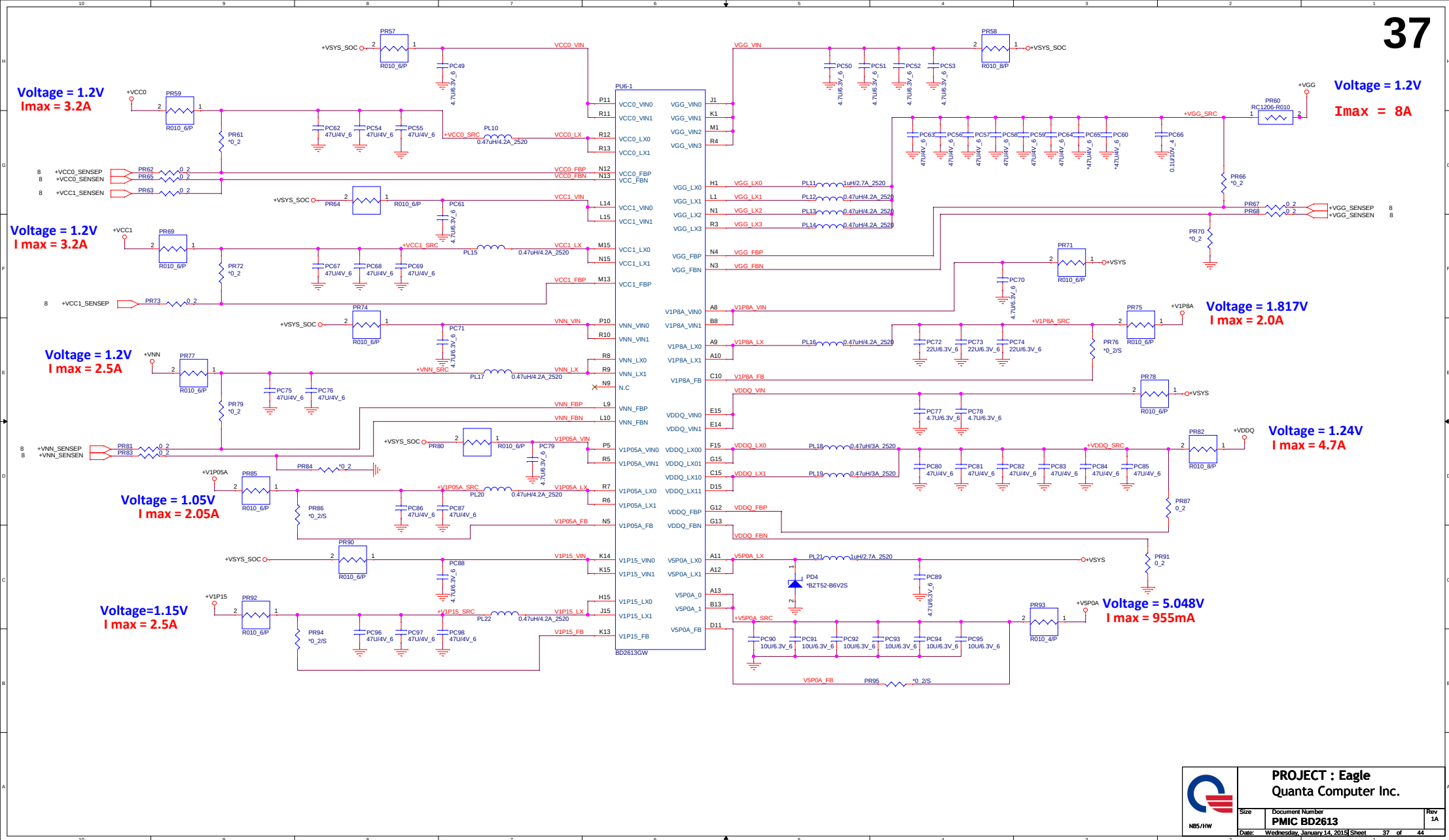


SoC I2C	Function	Address	Speed
0	EC	ACPI / WMI : 0x76 (7bit) 0xEC (8bit) Flash : 0x5B (7bit) 0xB6 (8bit)	400k Hz
1	Audio	33h	400k Hz
3	Camera	8M-->IMX219 0x20/0x21 (8bit) 2m-->OV2724 SiD=0, 0x6C (8bit)	400k Hz
4	HDMI	NA	NA
5	Touch Screen	0X4B	400k Hz
6	PMIC	0x5E (8bit) 0x6E (8bit)	HS mode (3.4 MHz)
7 (NFC)	NFC	0 X 28 (7 bit)	HS mode (3.4 MHz)
ISH	Combo Sensor	acceleration and magnetic (8bit) Read 0x3B Write 0x3A Angular--> Read 0xD7 Write 0xD6	400k Hz
ISH	ALS	Ra PD: 0x10 (default) Ra PU: 0x48	400k Hz
ISH	SAR	0 x 28 (Check table 8) (7bit)	400k Hz

EC I2C	Function	Address	Speed
0	SoC		HS-mode
2	TPS65132	W: 0x7C R: 0x7D (8bit)	
	Battery		
	Charger	W: 0x6A R: 0x6B (8bit)	400k Hz
3	POGO		
1	PD		
	DOCK		
	Moto Driver	W:0xB4 R: 0xB5 (8bit)	400k Hz



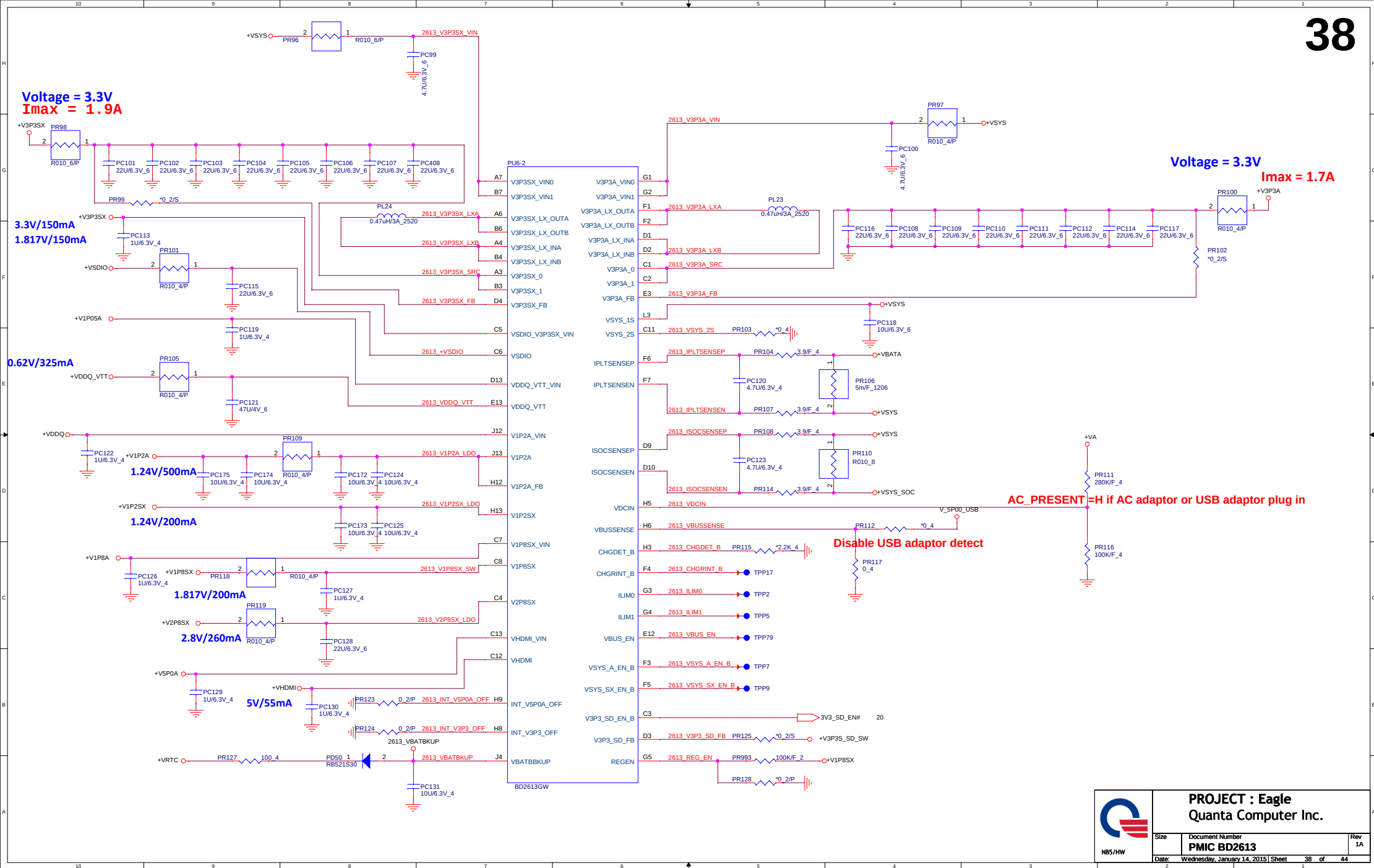


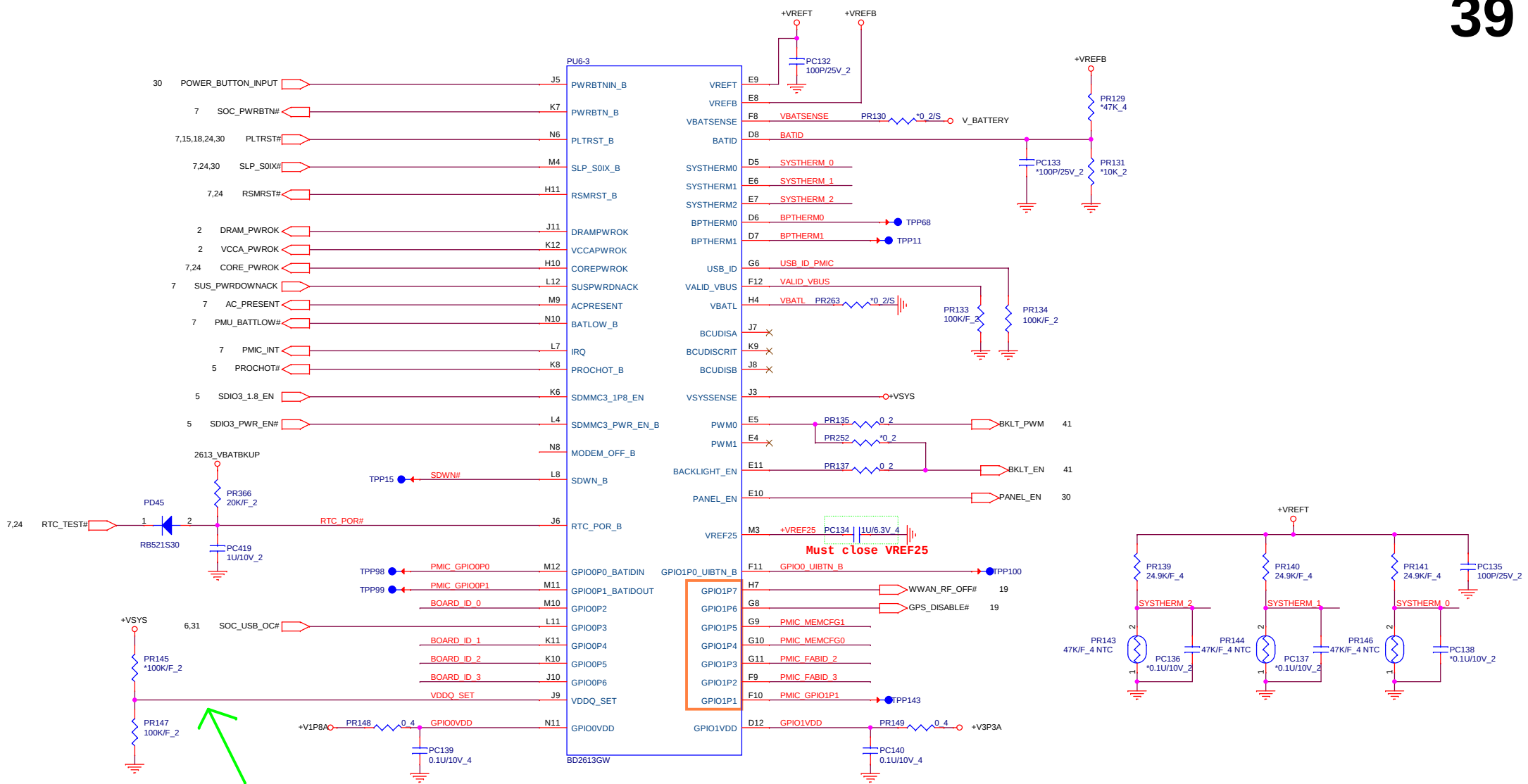


3.3V/150mA
1.817V/150mA

adaptor or USB adaptor plug in

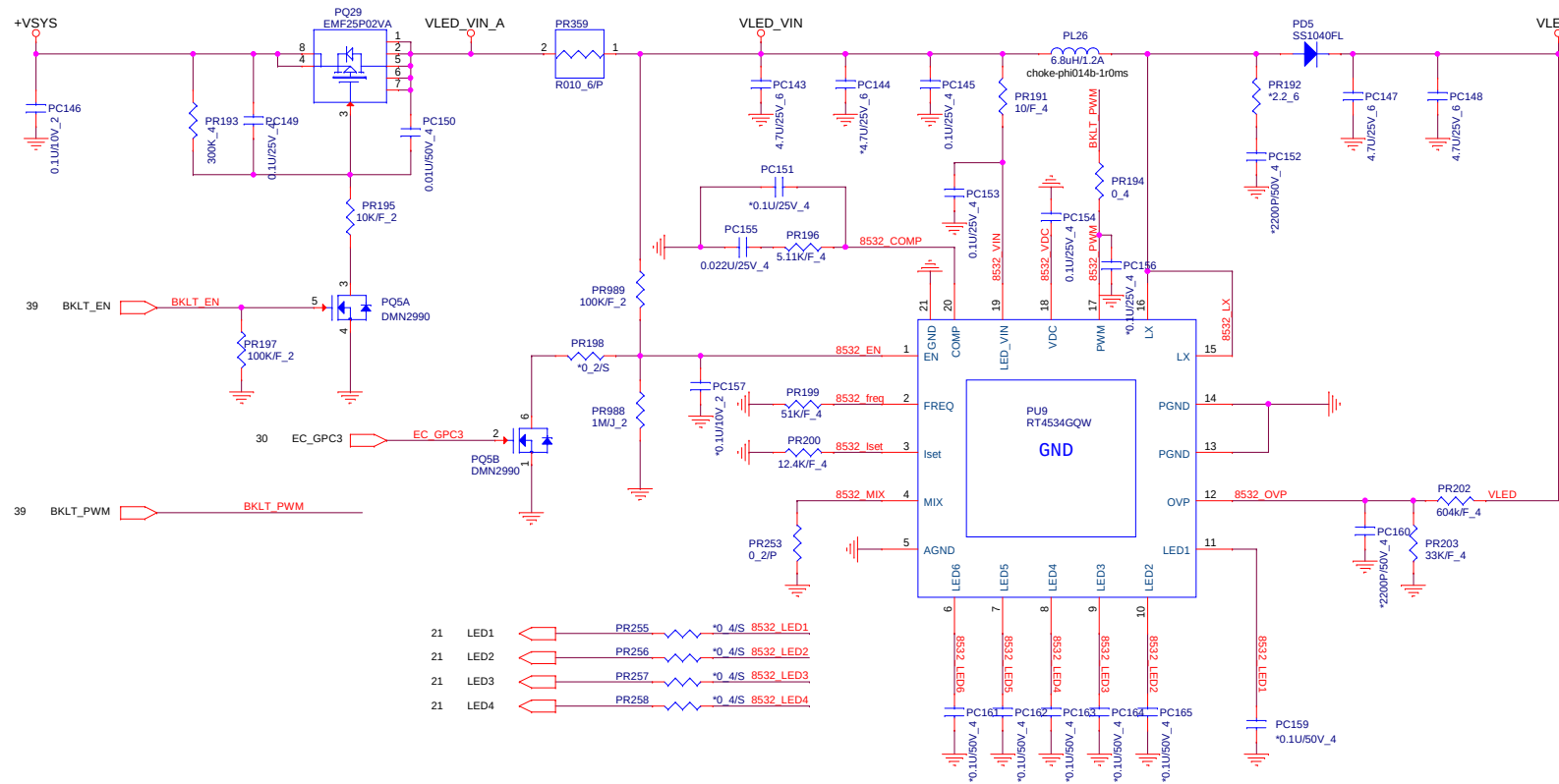
Disable USB adaptor detect



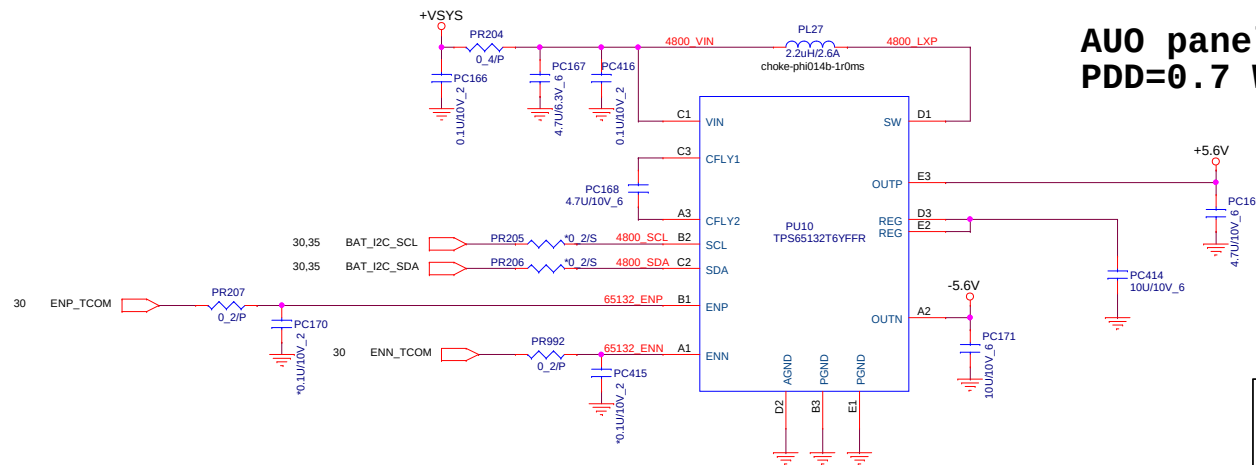




AU0 panel
LED: 6S4P
Vf=3.2V, If=20mA
PLED=1.48Watt
OVP=23.2V



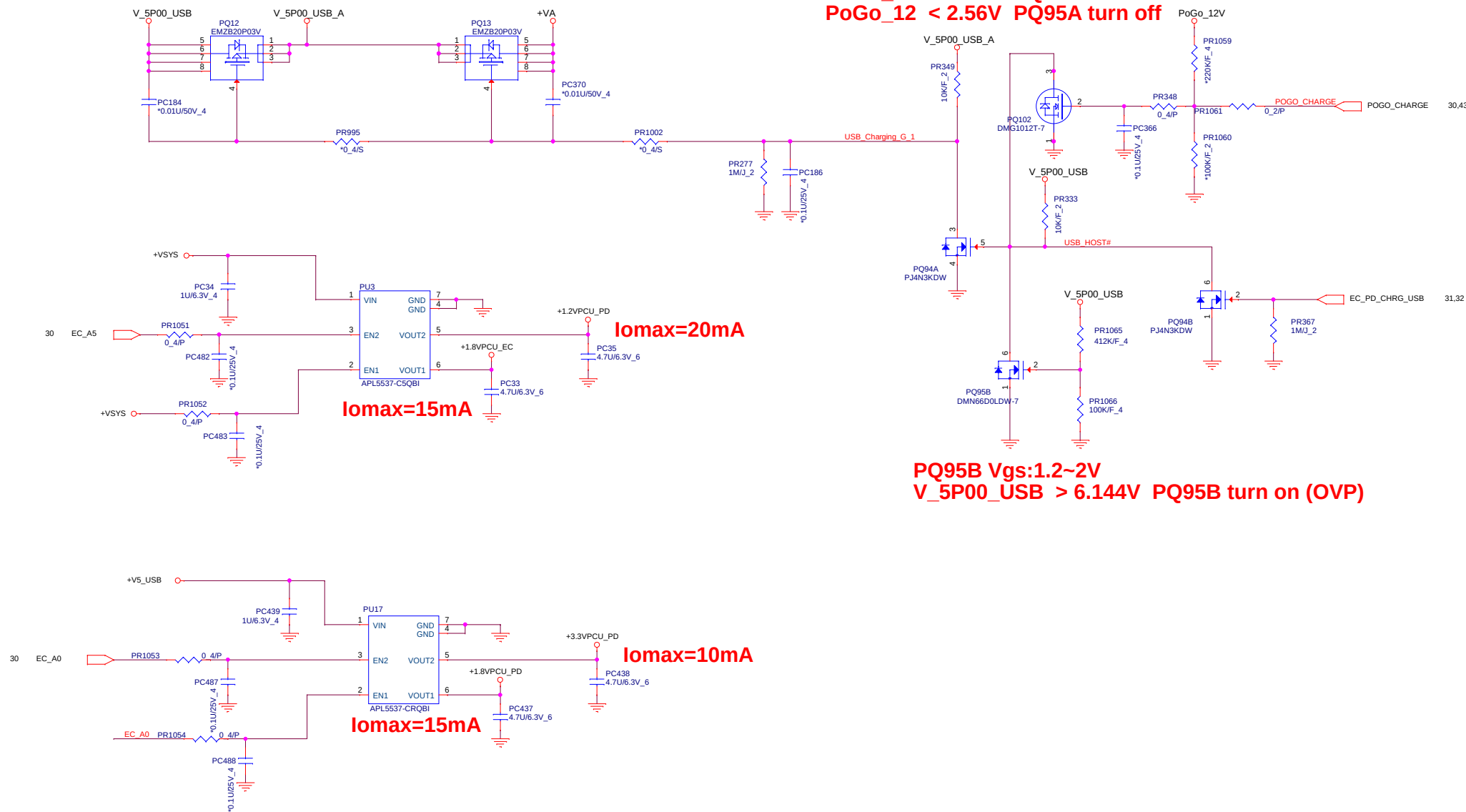
AU0 panel
PDD=0.7 Watt

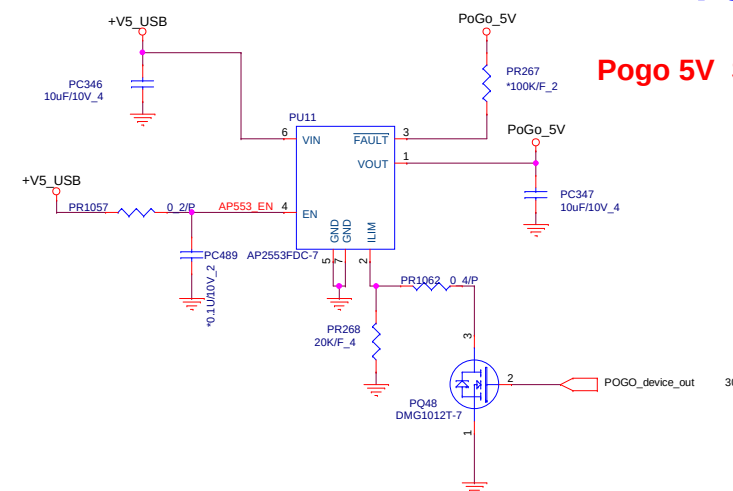


PROJECT : Eagle
Quanta Computer Inc.

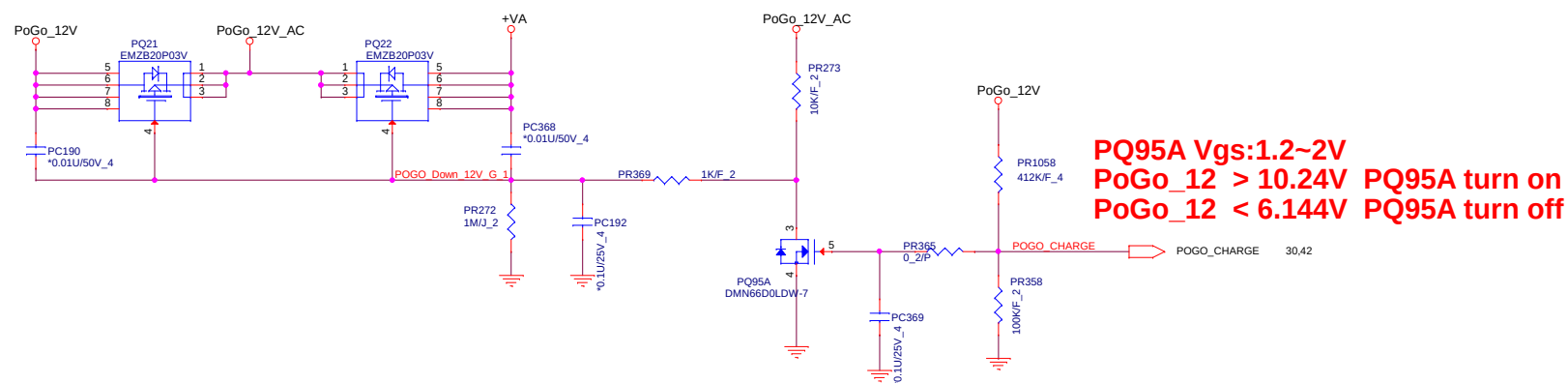
Size	Document Number	Rev
	LED Driver	1A
Date:	Wednesday, January 14, 2015	Sheet 41 of 44

PQ102 Vgs:0.5~1V
 PoGo_12 > 5.12V PQ95A turn on
 PoGo_12 < 2.56V PQ95A turn off





Pogo 12V downstream SW5



SWI_POGO_DET=1V~3V

SW5 on



PROJECT : Eagle
Quanta Computer Inc.

Size	Document Number USB/POGO switch	Rev 1
Date:	Wednesday, January 14, 2015	Sheet 43 of 44

